

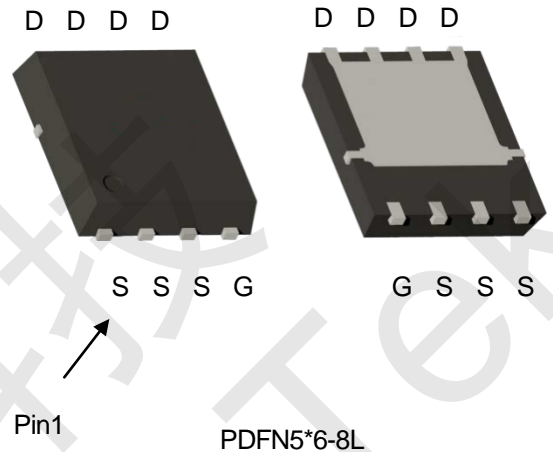


## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Feature

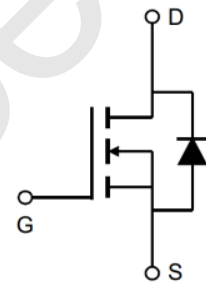
- 30V/100A  
 $R_{DS(ON)} = 2.4m\Omega$  (typ.) @  $V_{GS} = 10V$   
 $R_{DS(ON)} = 3.5m\Omega$  (typ.) @  $V_{GS} = 4.5V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available  
(RoHS Compliant)

### HYG030N03LQ1C2 Pin Description



### HYG030N03LQ1C2 Applications

- Battery Protection
- Load Switch



Single N-Channel MOSFET

### HYG030N03LQ1C2 Ordering and Marking Information

<p>C2 G030N03 XYMXXXXXX</p>	<p>Package Code C2: PDFN5*6-8L</p> <p>Date Code XYMXXXXXX</p>
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Note: This lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish; which are fully compliant with RoHS. This lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. Our company defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Our company reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.



## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
T <sub>J</sub>	Maximum Junction Temperature		-55 to 175	°C
T <sub>STG</sub>	Storage Temperature Range		-55 to 175	°C
I <sub>S</sub>	Source Current-Continuous(Body Diode)	Tc=25°C	100	A
Mounted on Large Heat Sink				
I <sub>DM</sub>	Pulsed Drain Current *	Tc=25°C	400	A
I <sub>D</sub>	Continuous Drain Current	Tc=25°C	100	A
		Tc=100°C	71	A
P <sub>D</sub>	Maximum Power Dissipation	Tc=25°C	62.5	W
		Tc=100°C	31.2	W
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case		2.4	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient **		50	°C/W
E <sub>AS</sub>	SinglePulsed-Avalanche Energy ***	L=0.3mH	148	mJ

Note: \* Repetitive rating; pulse width limited by max.junction temperature.

\*\* Surface mounted on FR-4 board.

\*\*\* Limited by T<sub>Jmax</sub>, starting T<sub>J</sub>=25°C, L = 0.3mH, V<sub>DS</sub>=24V., V<sub>GS</sub>=10V.

### HYG030N03LQ1C2 Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG030N03LQ1			Unit
			Min	Typ.	Max	
Static Characteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	30	-	-	V
I <sub>DSS</sub>	Drain-to-Source Leakage Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	μA
		T <sub>J</sub> =100°C	-	-	50	μA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1.0	1.4	3.0	V
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	100	nA
R <sub>DS(ON)*</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =20A	-	2.4	3.0	mΩ
		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =20A	-	3.5	4.2	mΩ
Diode Characteristics						
V <sub>SD</sub> *	Diode Forward Voltage	I <sub>SD</sub> =20A, V <sub>GS</sub> =0V	-	0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =20A, dI <sub>SD</sub> /dt=100A/μs	-	12.6	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	5.3	-	nC



## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Electrical Characteristics (Cont.) (T<sub>c</sub> =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG030N03LQ1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	2.6	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, Frequency=1.0MHz	-	1886	-	pF
C <sub>oss</sub>	Output Capacitance		-	306	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	294	-	
t <sub>d(ON)</sub>	Turn-on Delay Time		V <sub>DD</sub> =15V,R <sub>G</sub> =2.5Ω, I <sub>DS</sub> =20A,V <sub>GS</sub> =10V	-	9.4	-
T <sub>r</sub>	Turn-on Rise Time	-		53.7	-	
t <sub>d(OFF)</sub>	Turn-off Delay Time	-		40.6	-	
T <sub>f</sub>	Turn-off Fall Time	-		58.8	-	
Gate Charge Characteristics						
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>DS</sub> =24V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	55.3	-	nC
Q <sub>g</sub> (4.5V)	Total Gate Charge		-	30.7	-	
Q <sub>gs</sub>	Gate-Source Charge		-	6.6	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	18.1	-	

Note: \*Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Typical Operating Characteristics

Figure 1: Power Dissipation

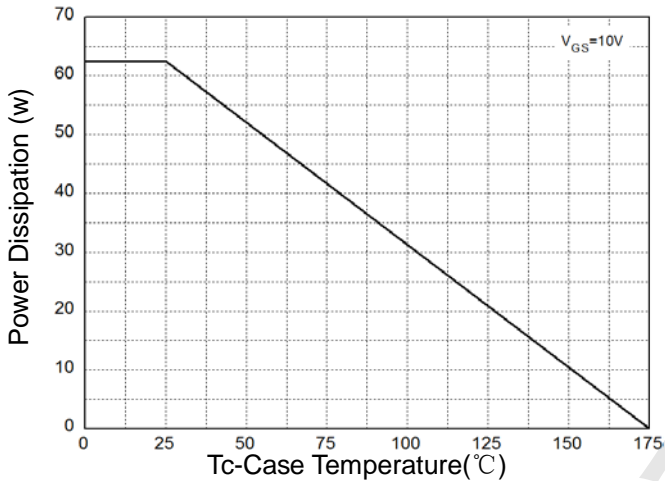


Figure 2: Drain Current

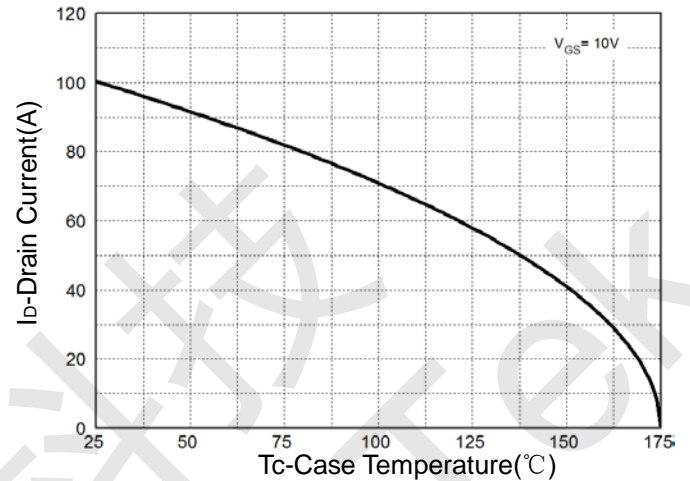


Figure 3: Safe Operation Area

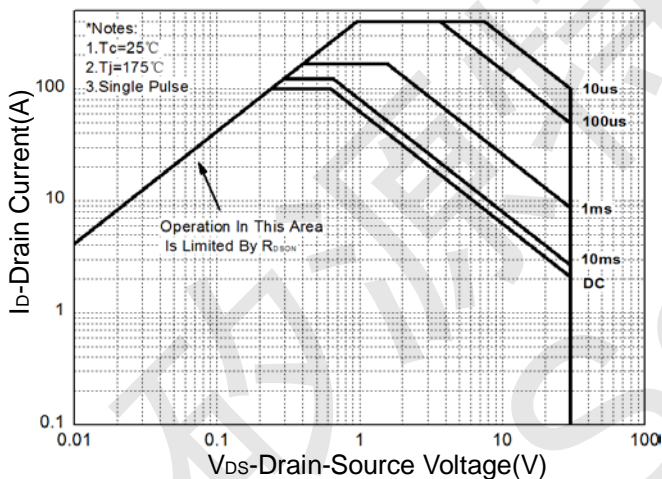


Figure 4: Thermal Transient Impedance

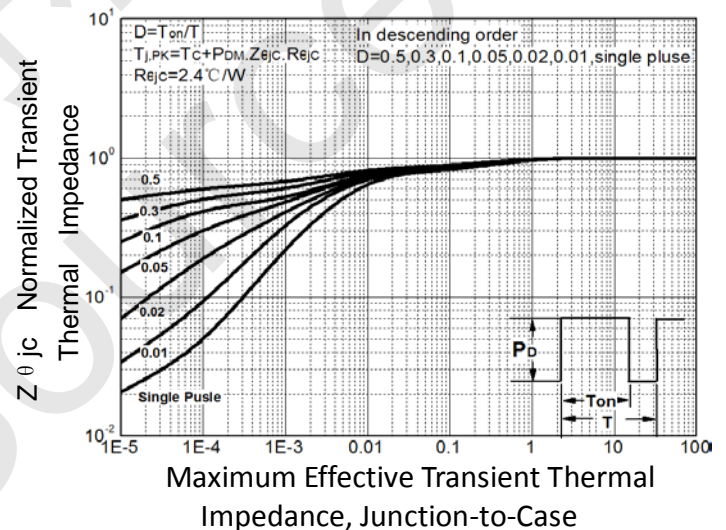


Figure 5: Output Characteristics

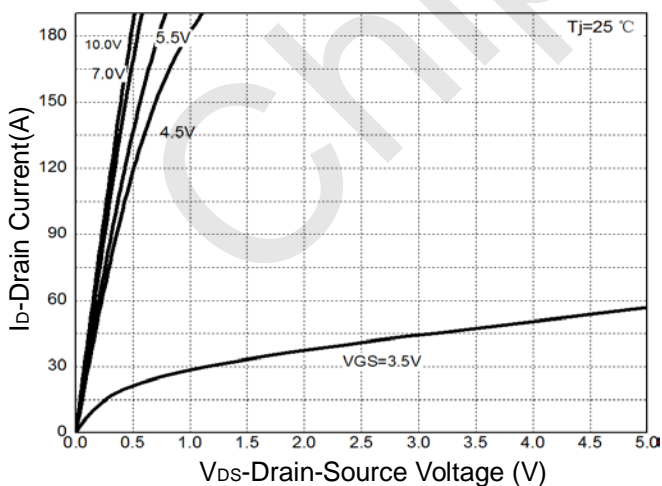
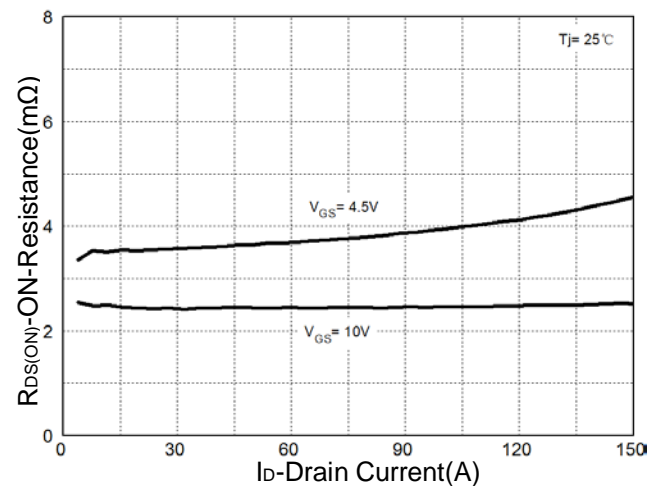


Figure 6: Drain-Source On Resistance







## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

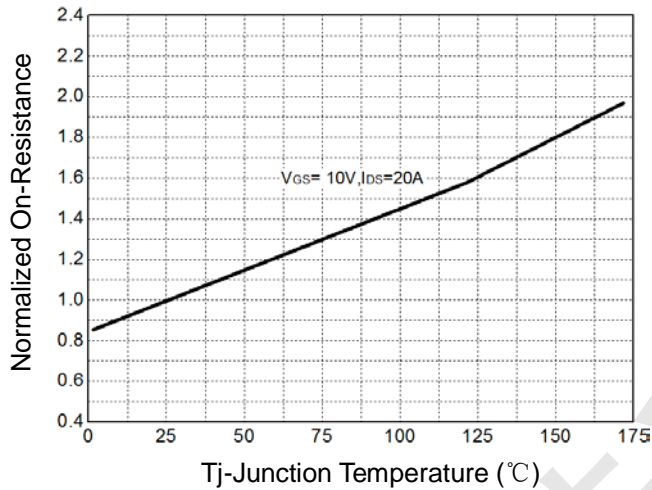


Figure 8: Source-Drain Diode Forward

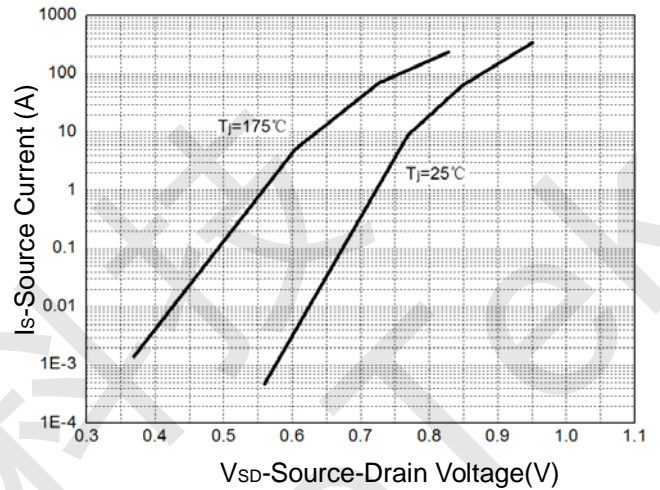


Figure 9: Capacitance Characteristics

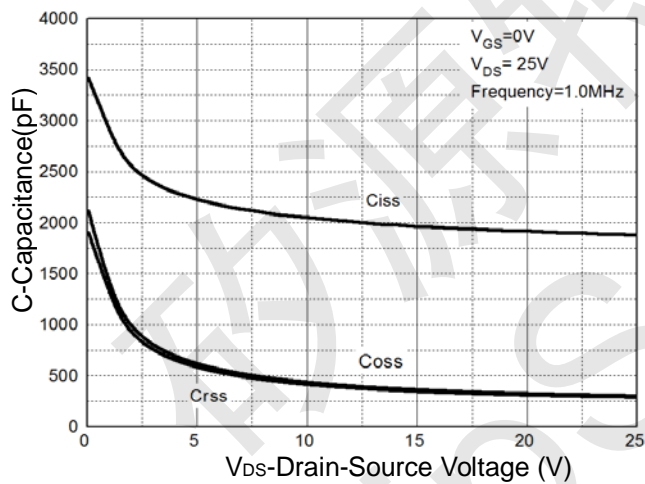
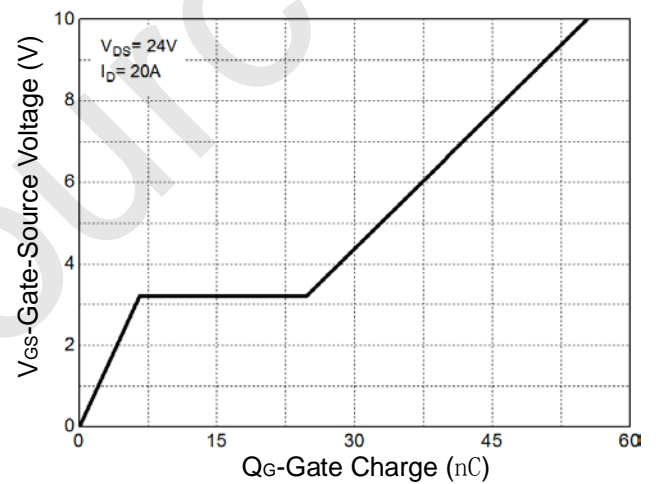


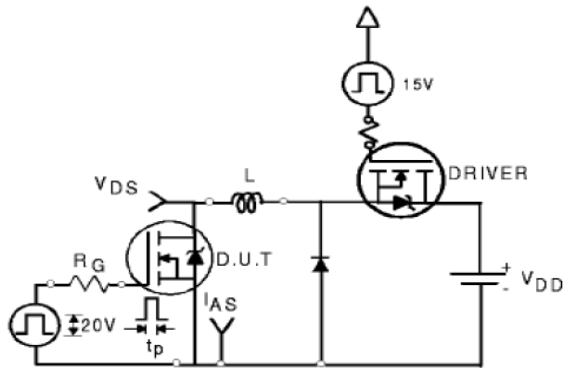
Figure 10: Gate Charge Characteristics



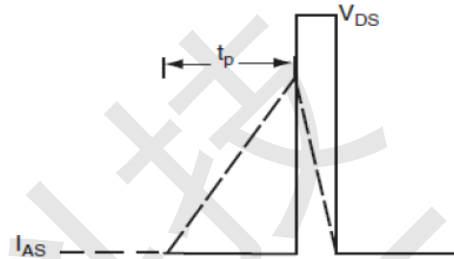


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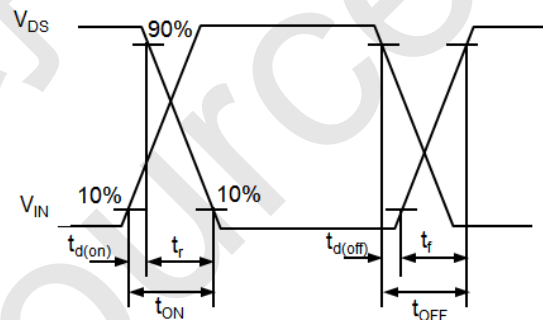
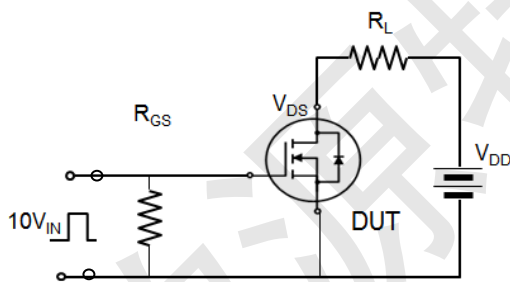
### Avalanche Test Circuit and Waveforms



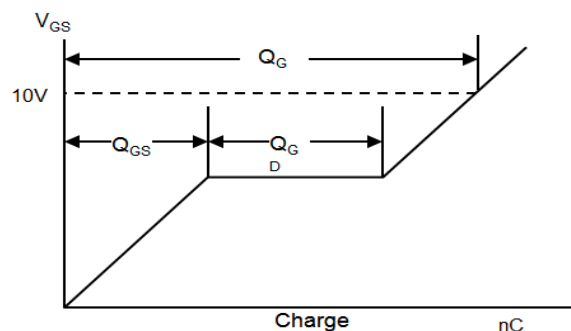
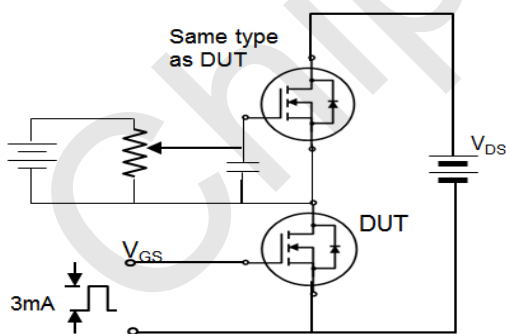
$$E_{AS} = \frac{1}{2} L I_{AS}^2$$



### Switching Time Test Circuit and Waveforms



### Gate Charge Test Circuit and Waveforms





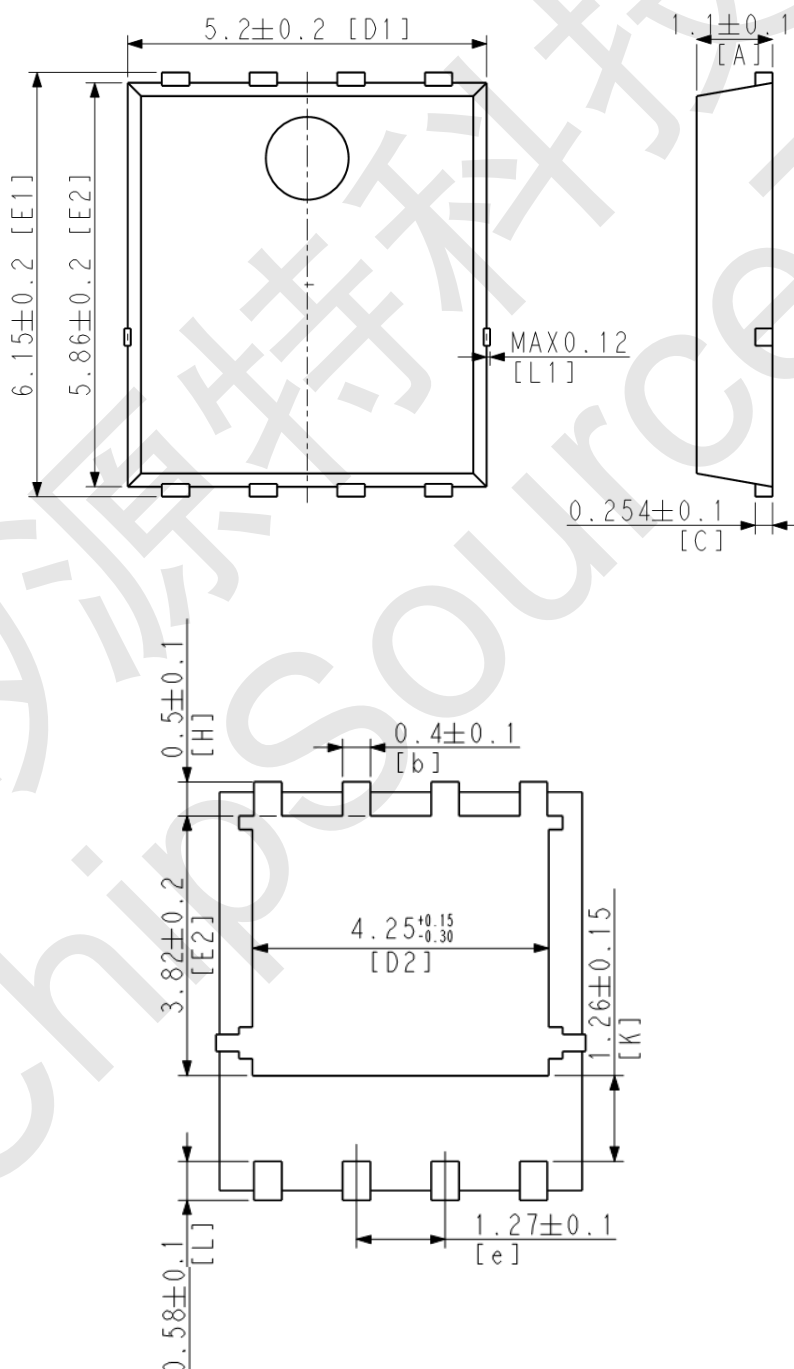
## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

### HYG030N03LQ1C2 Device Per Unit

Package Type	Unit	Quantity
PDFN5*6-8L	Reel	5000

### HYG030N03LQ1C2 Package Information

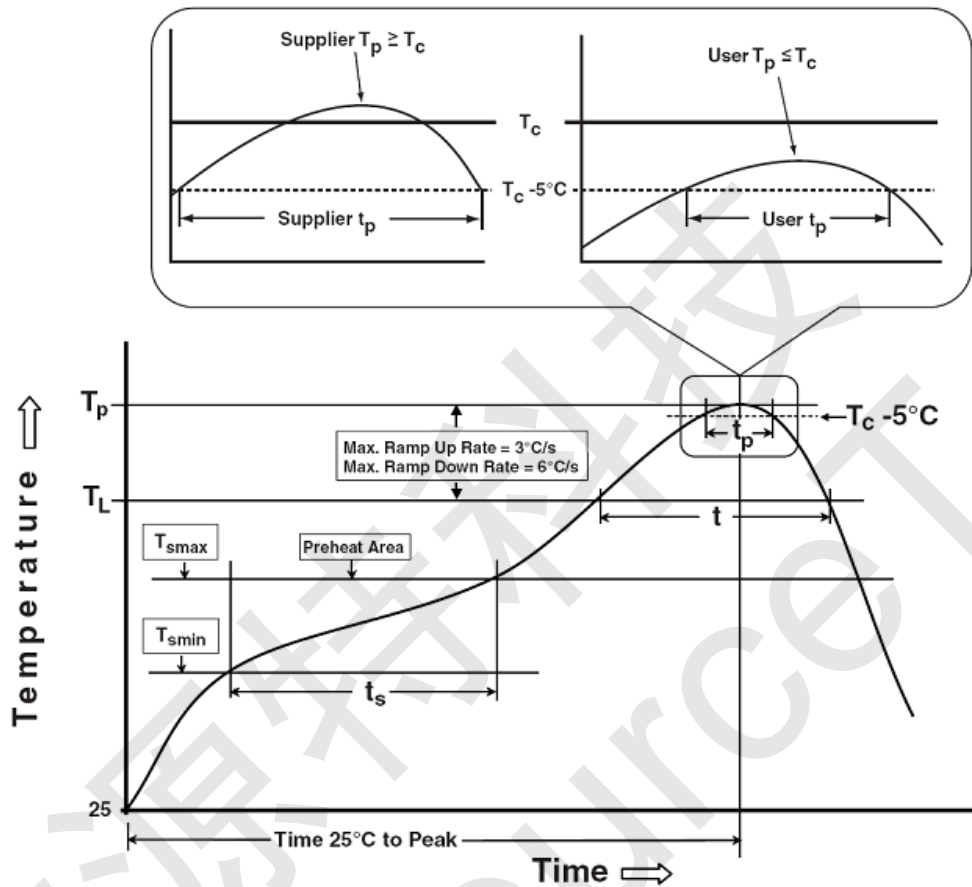
PDFN5\*6-8L





### HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

#### HYG030N03LQ1C2 Classification Profile



#### HYG030N03LQ1C2 Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100 °C	150 °C
Temperature max ( $T_{smax}$ )	150 °C	200 °C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217 °C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.		





## HYG030N03LQ1C2 Single N-Channel Enhancement Mode MOSFET

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## HYG030N03LQ1C2 Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
PRECON	JESD-22, A113	30°C/60%/192Hrs
HTRB	JESD-22, A108	168Hrs/500Hrs/1000Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 Hrs/500Hrs/1000Hrs, V <sub>gs</sub> 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C