



CST4913 Dual P-Ch 20V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

CST4913 Product Summary

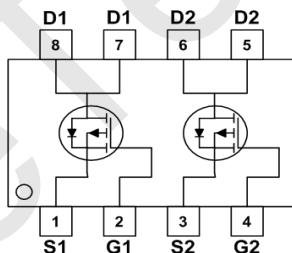
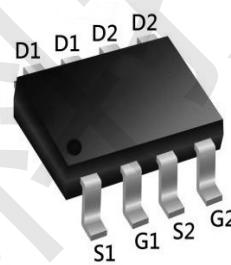


BVDSS	RDS(ON)	ID
-20V	26mΩ	-9.0A

CST4913 Description

The CST4913 is the high cell density trenched P-ch MOSFETs, which provides excellent RDS(ON) and efficiency for most of the small power switching and load switch applications. The CST4913 meet the RoHS and Green Product requirement with full function reliability approved.

CST4913 SOP8 Pin Configurations



CST4913 Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	-9.0	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	-5.0	A
I _{DM}	Pulsed Drain Current ²	-30	A
P _D @T _A =25°C	Total Power Dissipation ³	1.31	W
P _D @T _A =70°C	Total Power Dissipation ³	0.84	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

CST4913 Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	125	°C/W
R _{θJA}	Thermal Resistance Junction-Ambient ¹ (t ≤10s)	---	---	°C/W



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CST4913 Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D = -250\mu\text{A}$	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}= -20\text{V}$, $V_{\text{GS}}=0\text{V}$,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}= \pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D = -250\mu\text{A}$	-0.4	-0.7	-1.0	V
$R_{\text{DS}(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{\text{GS}}= -4.5\text{V}$, $I_D = -4.1\text{A}$	-	26	33	$\text{m}\Omega$
		$V_{\text{GS}}= -2.5\text{V}$, $I_D = -3\text{A}$	-	35	51	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}= -10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$	-	830	-	pF
C_{oss}	Output Capacitance		-	132	-	pF
C_{rss}	Reverse Transfer Capacitance		-	85	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}= -10\text{V}$, $I_D = -2\text{A}$, $V_{\text{GS}}= -4.5\text{V}$	-	8.8	-	nC
Q_{gs}	Gate-Source Charge		-	1.4	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1.9	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}= -10\text{V}$, $I_D = -3.3\text{A}$, $R_G= 1\Omega$, $V_{\text{GEN}}= -4.5\text{V}$	-	10	-	ns
t_r	Turn-on Rise Time		-	32	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	50	-	ns
t_f	Turn-off Fall Time		-	51	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	-9.0	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-16	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s = -4.1\text{A}$	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$



CST4913 Typical Performance Characteristics

Figure1: Output Characteristics

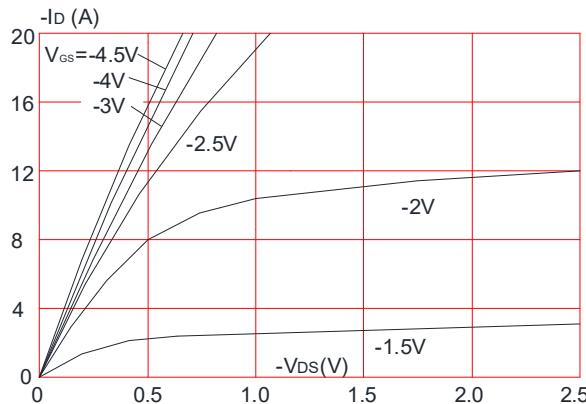


Figure 2: Typical Transfer Characteristics

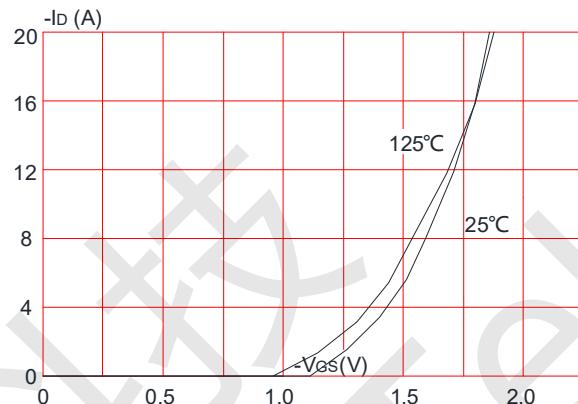


Figure 3: On-resistance vs. Drain Current

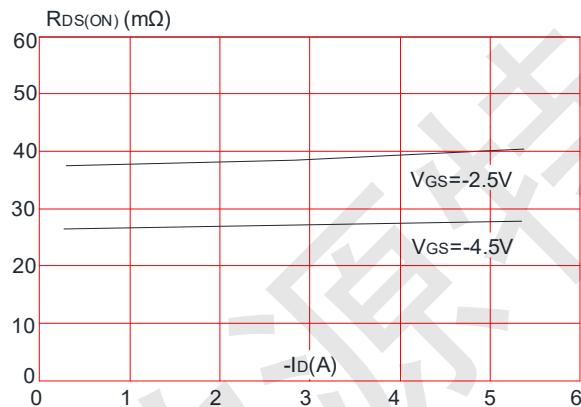


Figure 4: Body Diode Characteristics

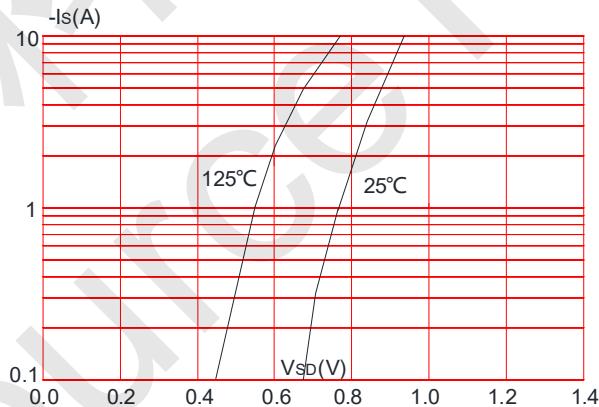


Figure 5: Gate Charge Characteristics

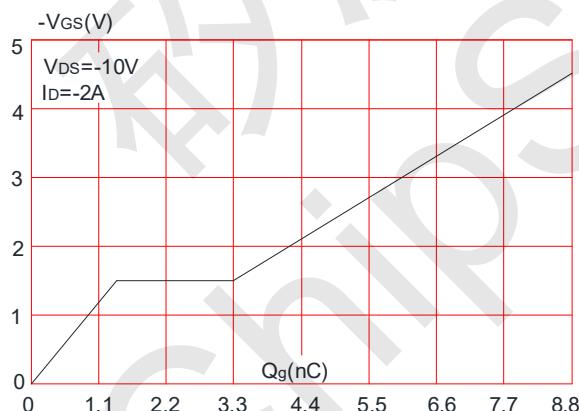
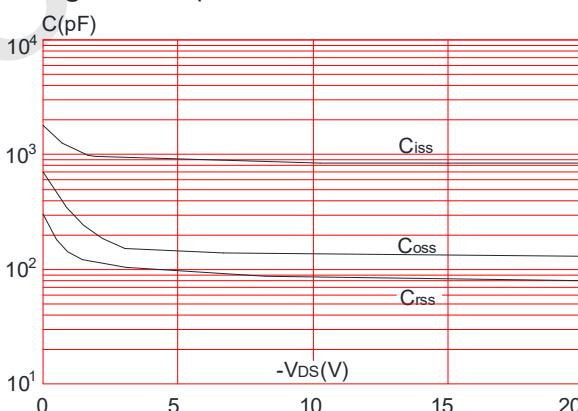


Figure 6: Capacitance Characteristics





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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

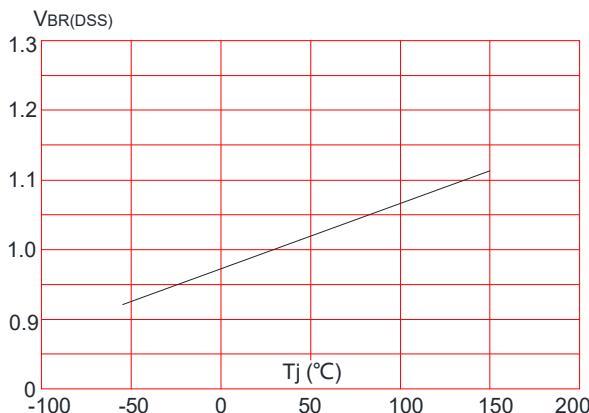


Figure 8: Normalized on Resistance vs. Junction Temperature

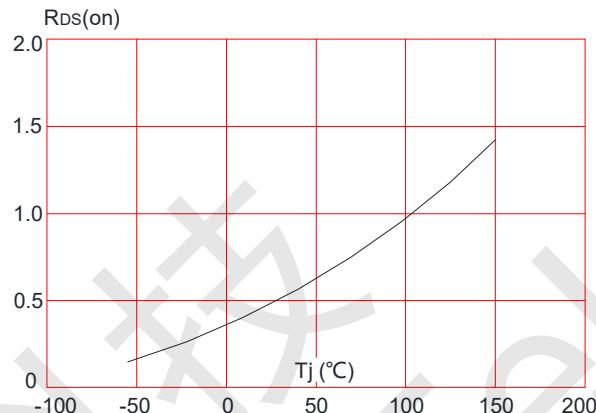


Figure 9: Maximum Safe Operating Area

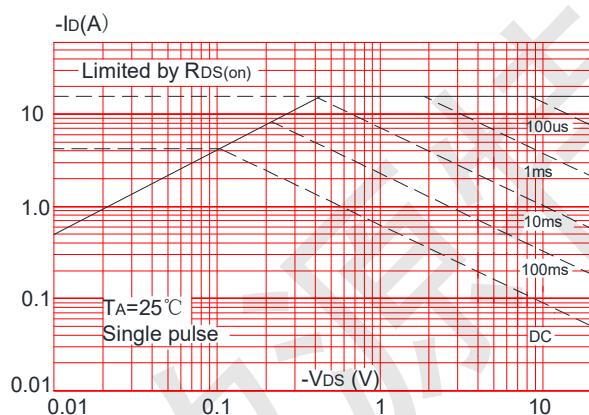


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

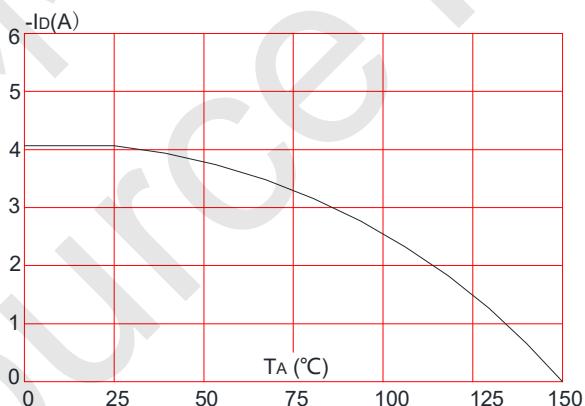
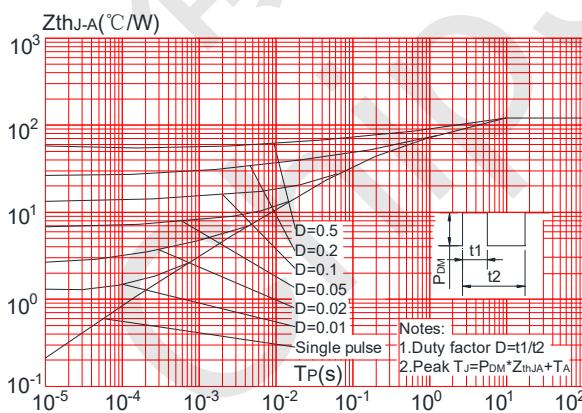


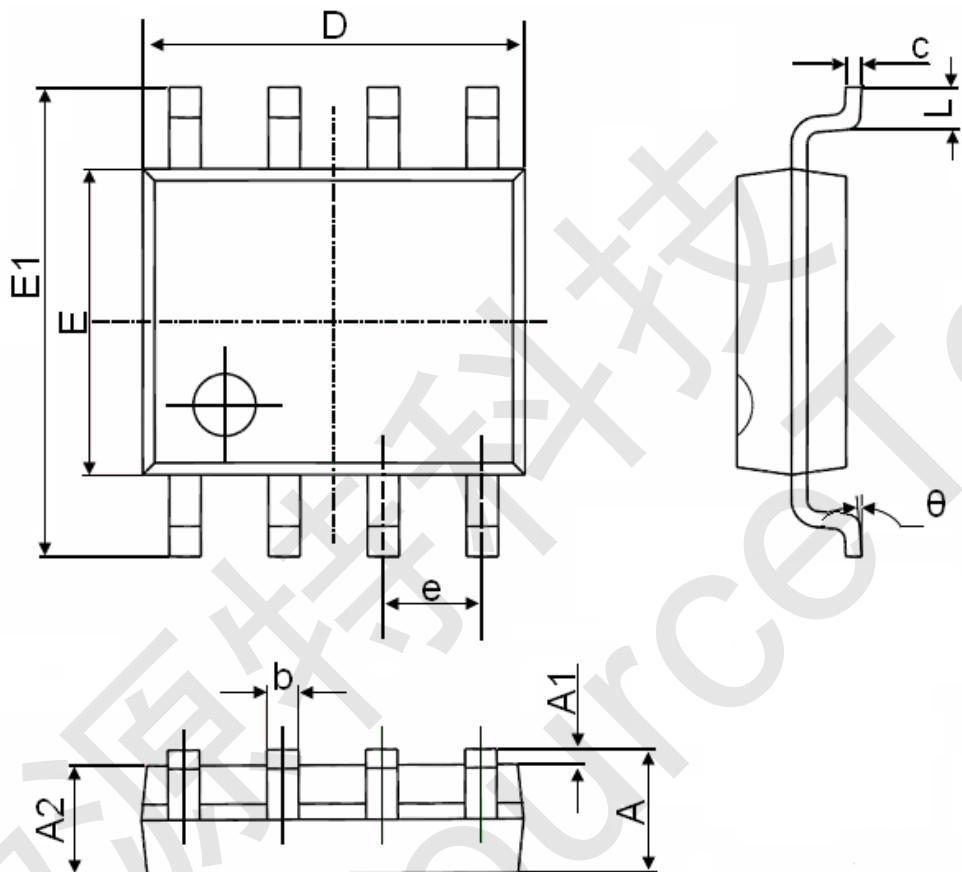
Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient





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CST4913 SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°