



### CST4822A Dual N-Ch 30V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



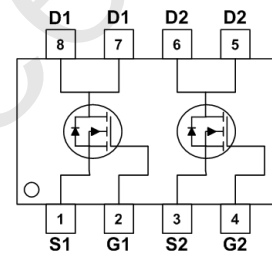
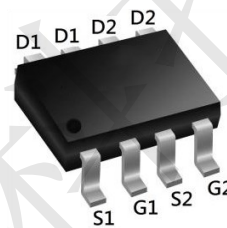
#### CST4822A Product Summary

BVDSS	RDSON	ID
30V	15mΩ	9.5A

#### CST4822A Description

The CST4822A is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The CST4822A meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### CST4822A Dual SOP8 Pin Configuration



#### CST4822A Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	9.5	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	5.8	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	37	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	12.1	mJ
$I_{AS}$	Avalanche Current	--	A
$P_D@T_A=25^\circ C$	Total Power Dissipation <sup>4</sup>	2.8	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

#### CST4822A Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	---	---	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	46	$^\circ C/W$



### CST4822A Dual N-Ch 30V Fast Switching MOSFETs

#### CST4822A Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> = 0V,	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.5	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	15	20	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	21	29	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHz	-	490	-	pF
C <sub>oss</sub>	Output Capacitance		-	79	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	61	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, I <sub>D</sub> =5.8A, V <sub>GS</sub> =10V	-	10	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	1.7	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	2.5	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =15V, I <sub>D</sub> =3A, V <sub>GS</sub> =10V, R <sub>REN</sub> =3Ω	-	6	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	15	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	17	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	17	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	9	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	36	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =9A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> =5A, di/dt=100A/μs	-	7	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	2	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, L=0.5mH, R<sub>g</sub>=25Ω, I<sub>AS</sub>=6A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



CST4822A Dual N-Ch 30V Fast Switching MOSFETs

CST4822A Typical Performance Characteristics

Figure 1: Output Characteristics

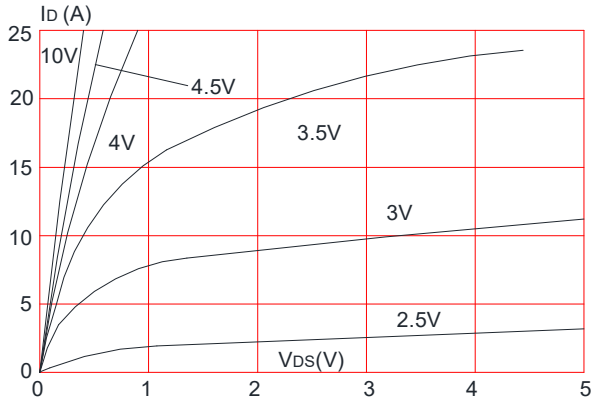


Figure 2: Typical Transfer Characteristics

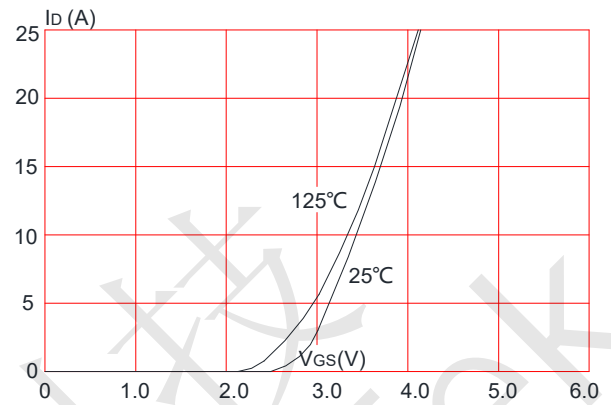


Figure 3: On-resistance vs. Drain Current

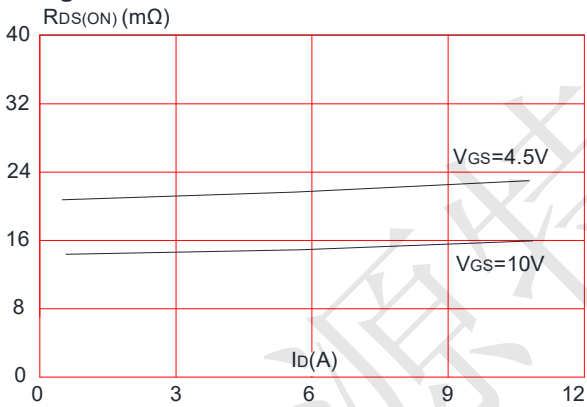


Figure 4: Body Diode Characteristics

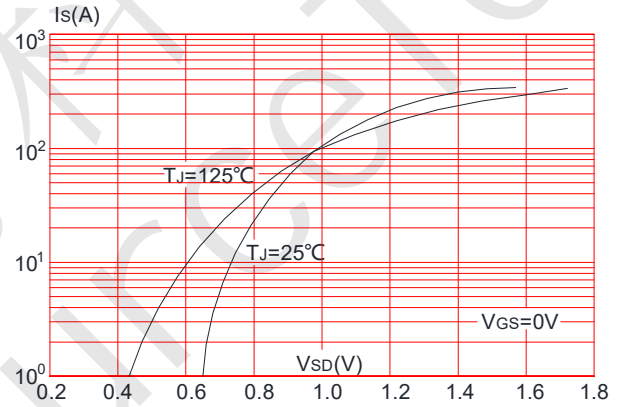


Figure 5: Gate Charge Characteristics

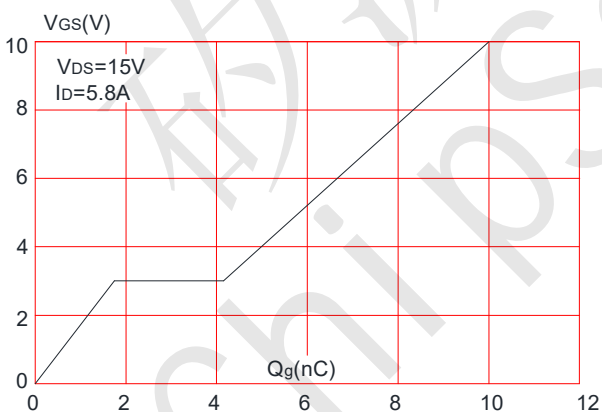
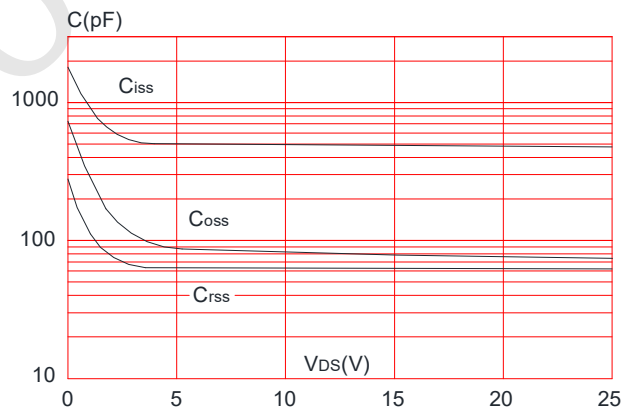


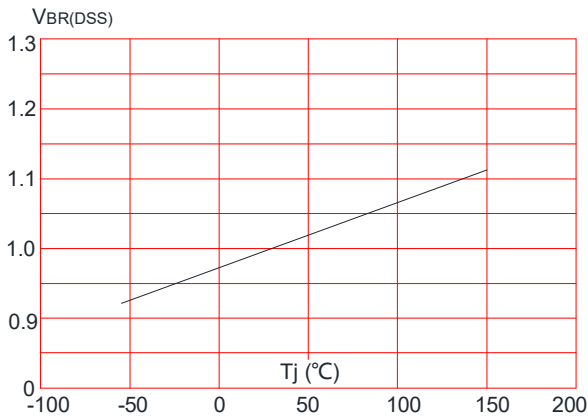
Figure 6: Capacitance Characteristics



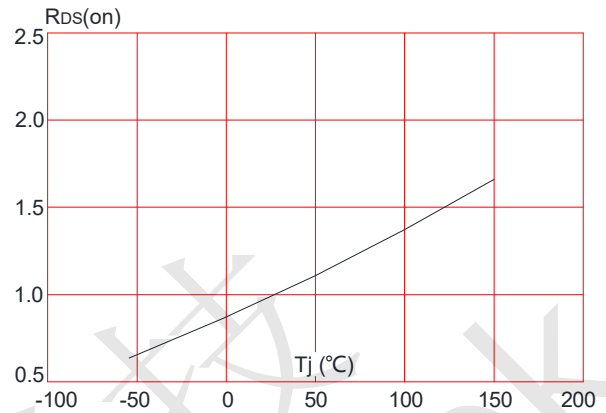


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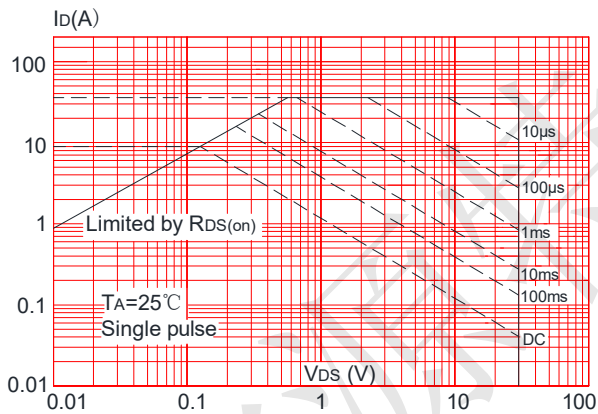
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



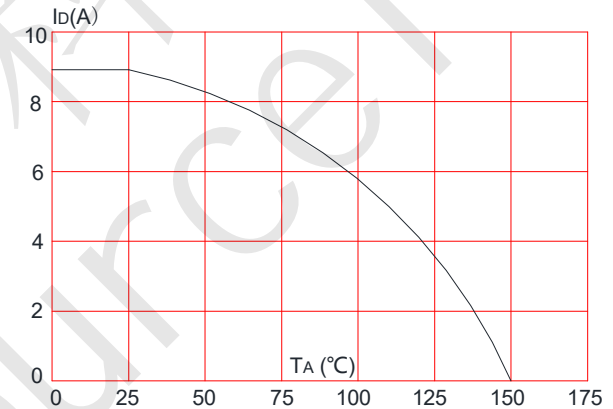
**Figure 8:** Normalized on Resistance vs. Junction Temperature



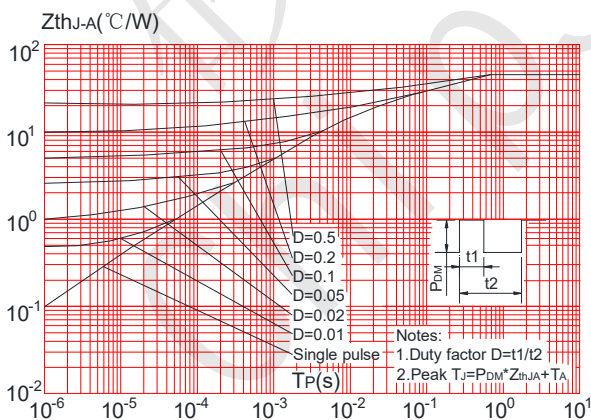
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



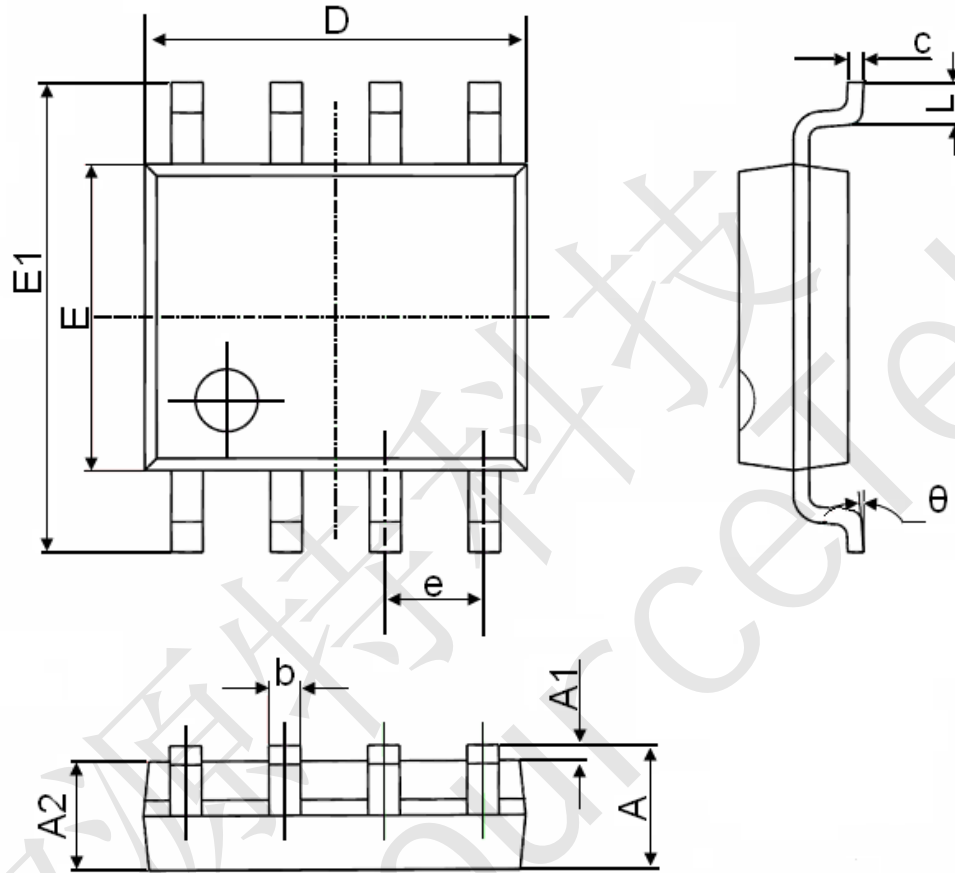
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient





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CST4822A Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°