



### CST40N02 N-Ch 20V Fast Switching MOSFETs

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

#### CST40N02 Product Summary



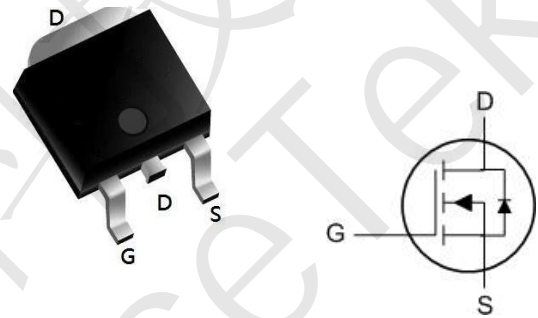
BVDSS	RDS(on)	ID
20V	8 mΩ	40A

#### CST40N02 Description

The CST40N02 is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications.

The CST40N02 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### CST40N02 TO252 Pin Configuration



#### CST40N02 Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	40	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	20	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	120	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	23	mJ
I <sub>AS</sub>	Avalanche Current	21	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	25	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

#### CST40N02 Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	7.5	°C/W



### CST40N02 N-Ch 20V Fast Switching MOSFETs

#### CST40N02 Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V,	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.75	1.2	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	-	8	11.2	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =10A	-	11.7	17.5	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1.0MHz	-	1000	-	pF
C <sub>oss</sub>	Output Capacitance		-	182	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	164	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, I <sub>D</sub> =15A, V <sub>GS</sub> =4.5V	-	15	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	2	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	5.2	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =10V, I <sub>D</sub> =15A, R <sub>GEN</sub> =3Ω, V <sub>GS</sub> =4.5V	-	9	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	25	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	37	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	14	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	40	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	120	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	-	-	1.2	V

- Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature  
 2. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=10V, V<sub>G</sub>=4.5V, L=0.5mH, R<sub>G</sub>=25Ω, I<sub>AS</sub>=9.6A  
 3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



## CST40N02 Typical Performance Characteristics

Figure 1: Output Characteristics

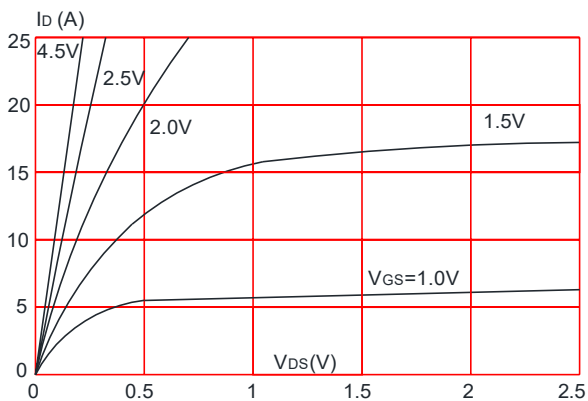


Figure 2: Typical Transfer Characteristics

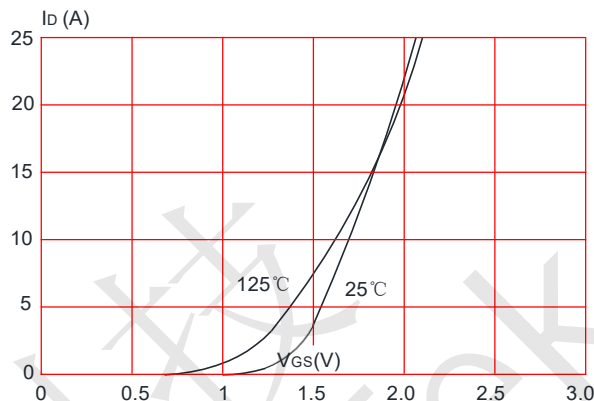


Figure 3: On-resistance vs. Drain Current

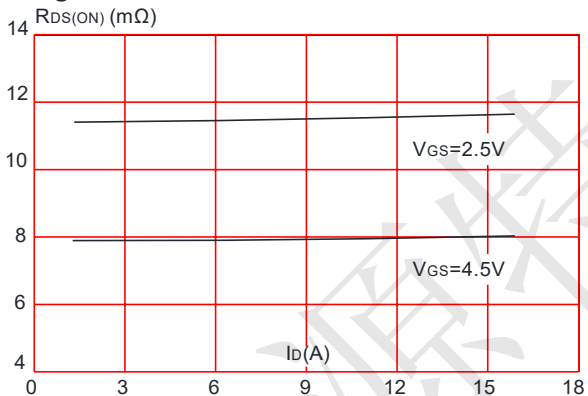


Figure 4: Body Diode Characteristics

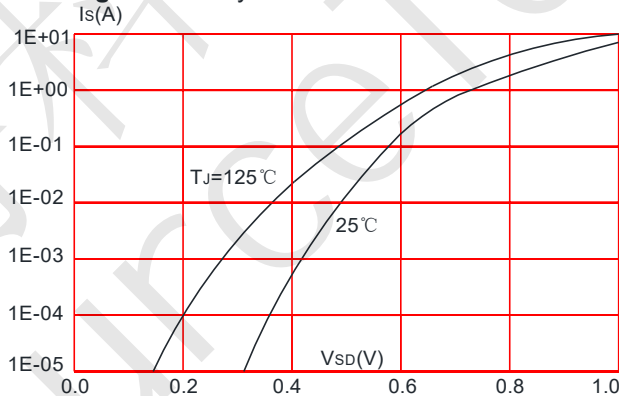


Figure 5: Gate Charge Characteristics

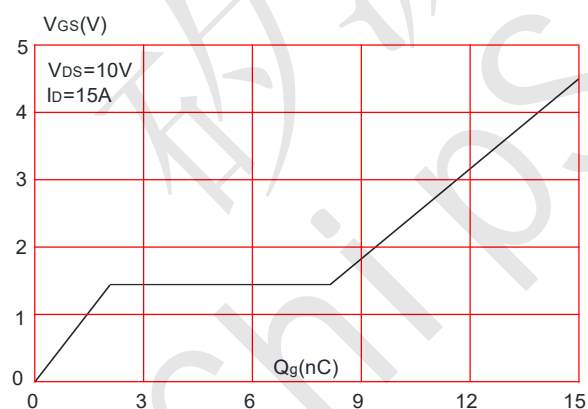
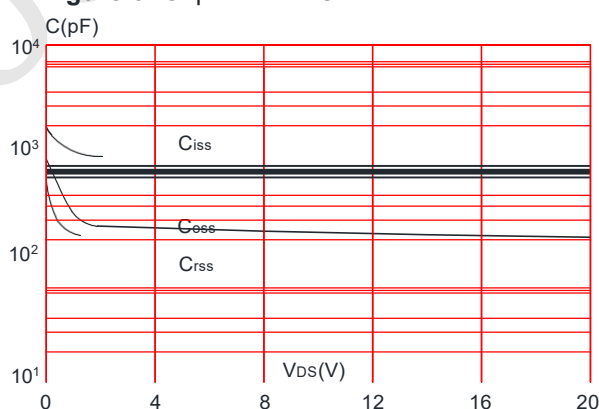


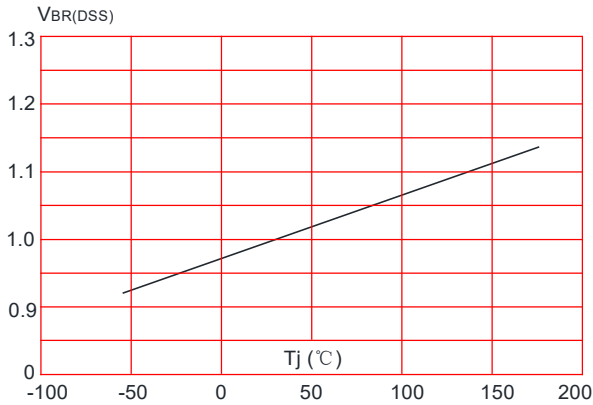
Figure 6: Capacitance Characteristics



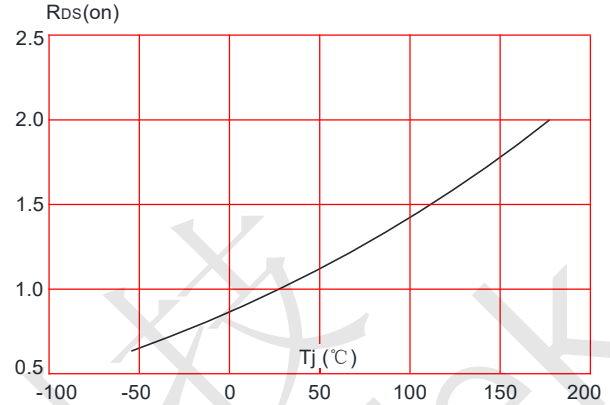


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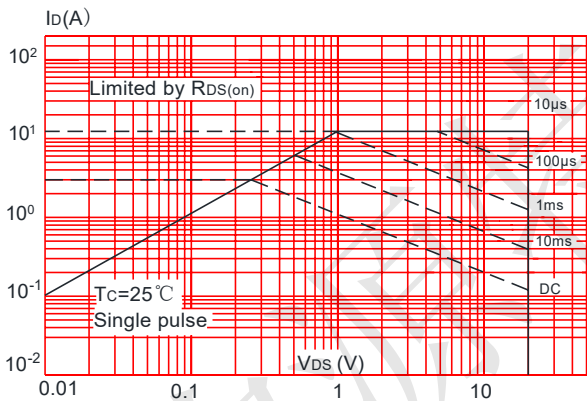
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



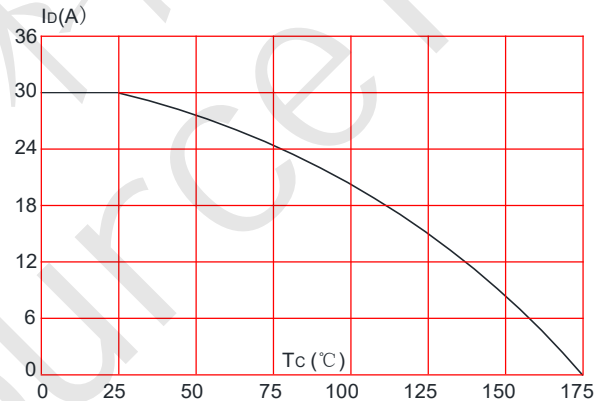
**Figure 8:** Normalized on Resistance vs. Junction Temperature



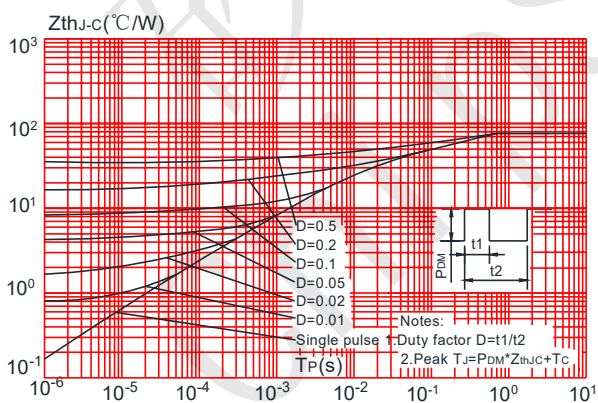
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



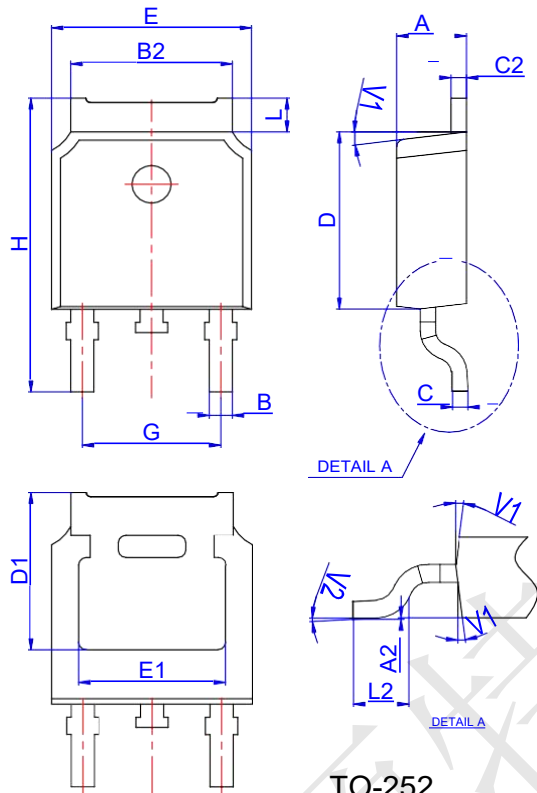
Maximum Effective Transient Thermal Impedance, Junction-to-Case





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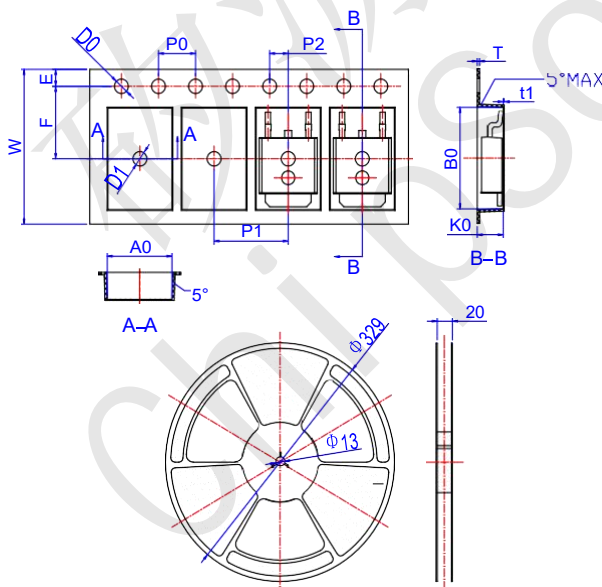
#### CST40N02 Package Mechanical Data-TO-252-4R



TO-252

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2		0°	6°		0°	6°

#### CST40N02 Reel Spectification-TO-252-4R



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583