



CST2309 P-Ch 60V Fast Switching MOSFETs

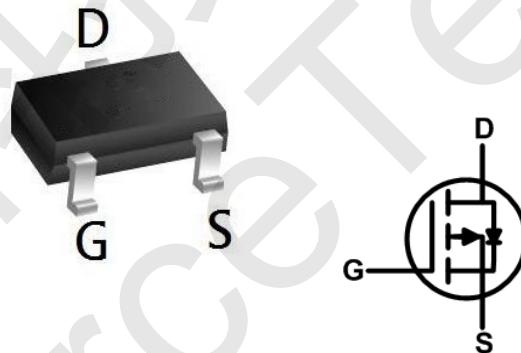
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

CST2309 Product Summary



BVDSS	RDS(on)	ID
-60V	160 mΩ	-2 A

CST2309 SOT23 Pin Configuration



CST2309 Description

The CST2309 is the high cell density trenched P-ch MOSFETs, which provides excellent RDS(on) and efficiency for most of the small power switching and load switch applications.

The CST2309 meet the RoHS and Green Product requirement with full function reliability approved.

CST2309 Absolute Maximum Rating

Parameter	Symbol	Value	Unit
Drain-Source voltage	V _{DSS}	-60	V
Gate-Source voltage	V _{GSS}	±20	
Continuous Drain Current	I _D	-2.0	A
Pulsed Drain Current ¹	I _{DM}	-5.2	A
Power Dissipation	P _D	1	W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55~150	°C
Thermal Resistance from Junction to Ambient ²	R _{θJA}	125	°C/W



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CST2309 Electrical Characteristics (T =25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ C$)	$V_{DS}=-60V, V_{GS}=0V$	--	--	-1	μA
	Zero Gate Voltage Drain Current($T_A=125^\circ C$)	$V_{DS}=-60V, V_{GS}=0V$	--	--	-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.5	-2.5	V
$R_{DS(ON)}$	Drain-Source On-State Resistance②	$V_{GS}=-10V, I_D=-2A$	--	160	200	$m\Omega$
$R_{DS(ON)}$	Drain-Source On-State Resistance②	$V_{GS}=-4.5V, I_D=-1A$	--	200	300	$m\Omega$

Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, f=1MHz$	--	310	--	pF
C_{oss}	Output Capacitance		--	22	--	pF
C_{rss}	Reverse Transfer Capacitance		--	15	--	pF
Q_g	Total Gate Charge	$V_{DS}=-30V, I_D=-2A, V_{GS}=-10V$	--	5.4	--	nC
Q_{gs}	Gate Source Charge		--	1.1	--	nC
Q_{gd}	Gate Drain Charge		--	1.6	--	nC

Switching Characteristics

$t_{d(on)}$	Turn on Delay Time	$V_{DD}=-30V, I_D=-2A, R_G=3.3\Omega, V_{GS}=-10V$	--	41	--	ns
t_r	Turn on Rise Time		--	22	--	ns
$t_{d(off)}$	Turn Off Delay Time		-	25	--	ns
t_f	Turn Off Fall Time		--	32	--	ns

Source Drain Diode Characteristics

I_{SD}	Source drain current(Body Diode)	$T_A=25^\circ C$	--	--	-2.0	A
V_{SD}	Forward on voltage②	$T_j=25^\circ C, I_{SD}=-2A, V_{GS}=0V$	--	-0.84	-1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width≤300μs, duty cycle≤2%.



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CST2309 Typical Characteristics

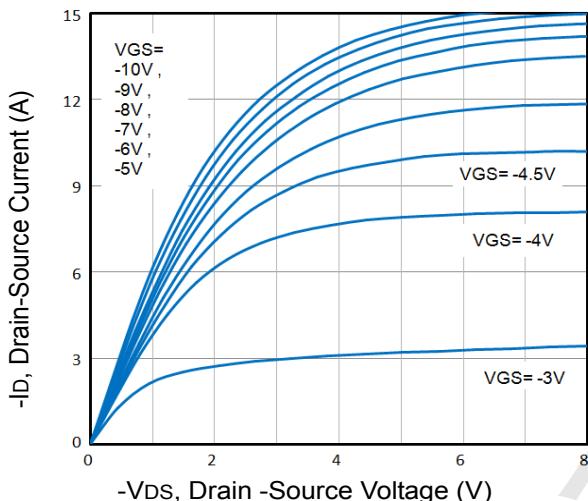


Fig1. Typical Output Characteristics

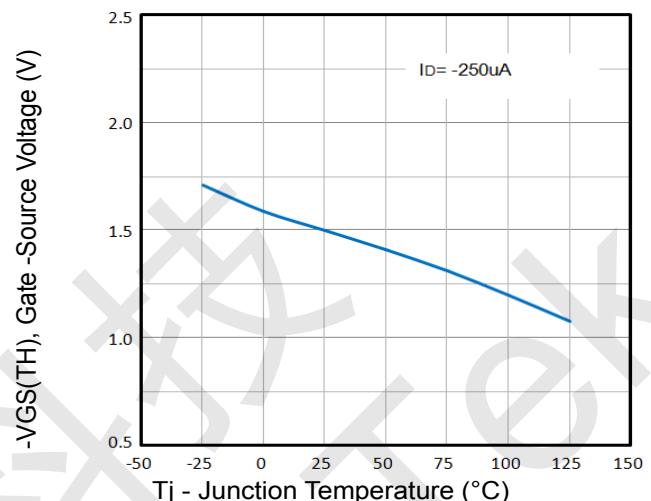


Fig2. Normalized Threshold Voltage Vs. Temperature

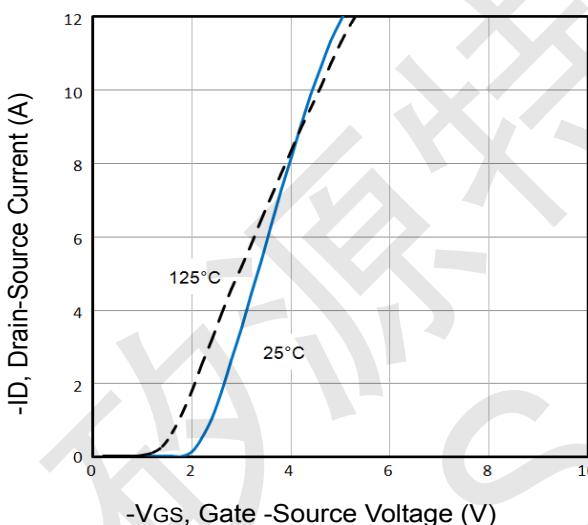


Fig3. Typical Transfer Characteristics

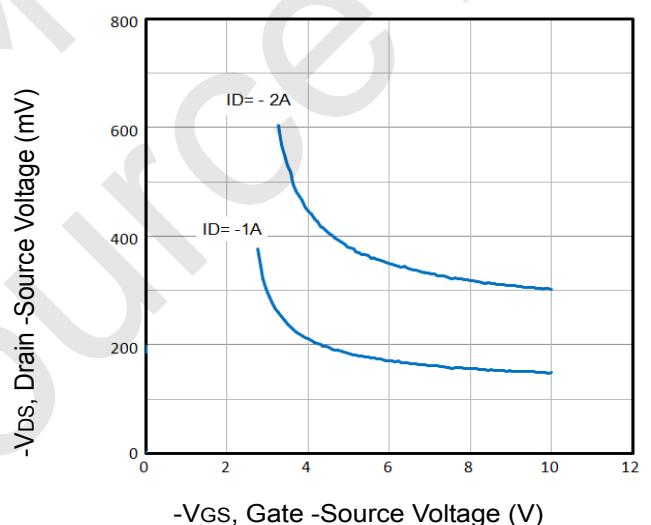


Fig4. Drain -Source Voltage vs Gate -Source Voltage

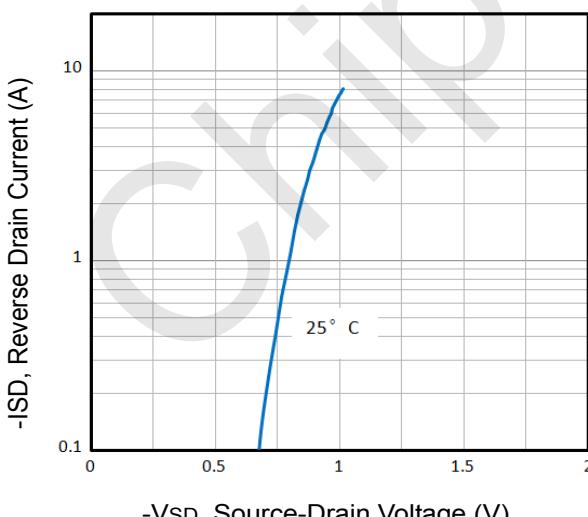


Fig5. Typical Source-Drain Diode Forward Voltage

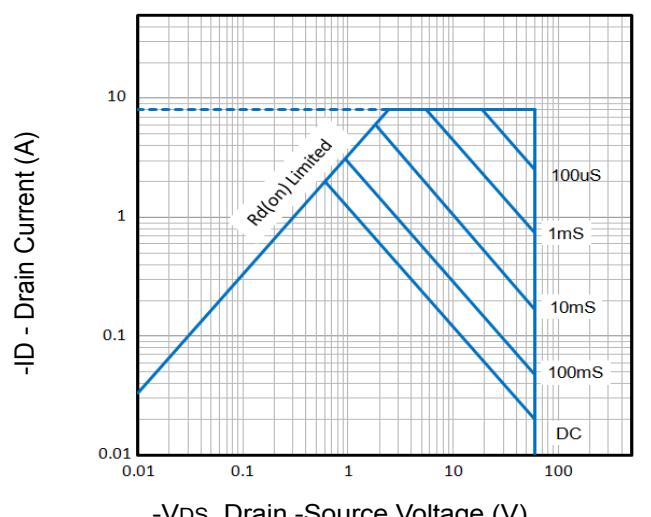


Fig6. Maximum Safe Operating Area



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CST2309 Typical Characteristics

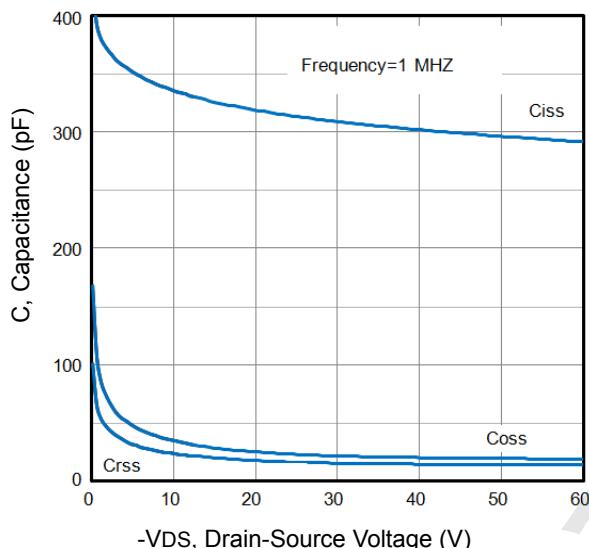


Fig7. Typical Capacitance Vs. Drain-Source Voltage

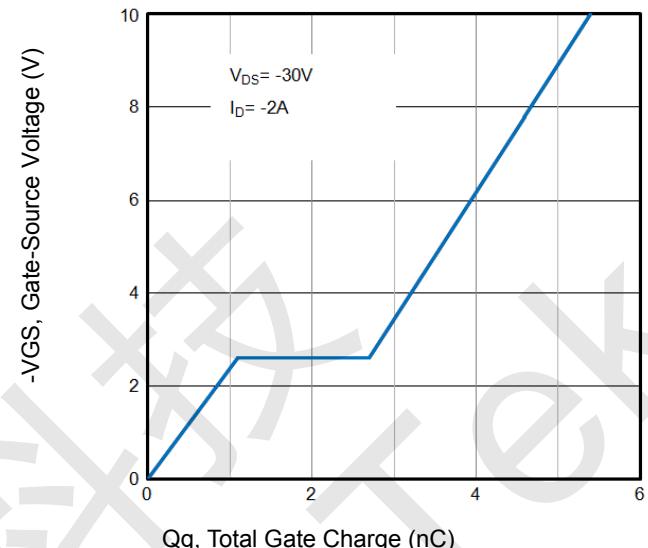


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

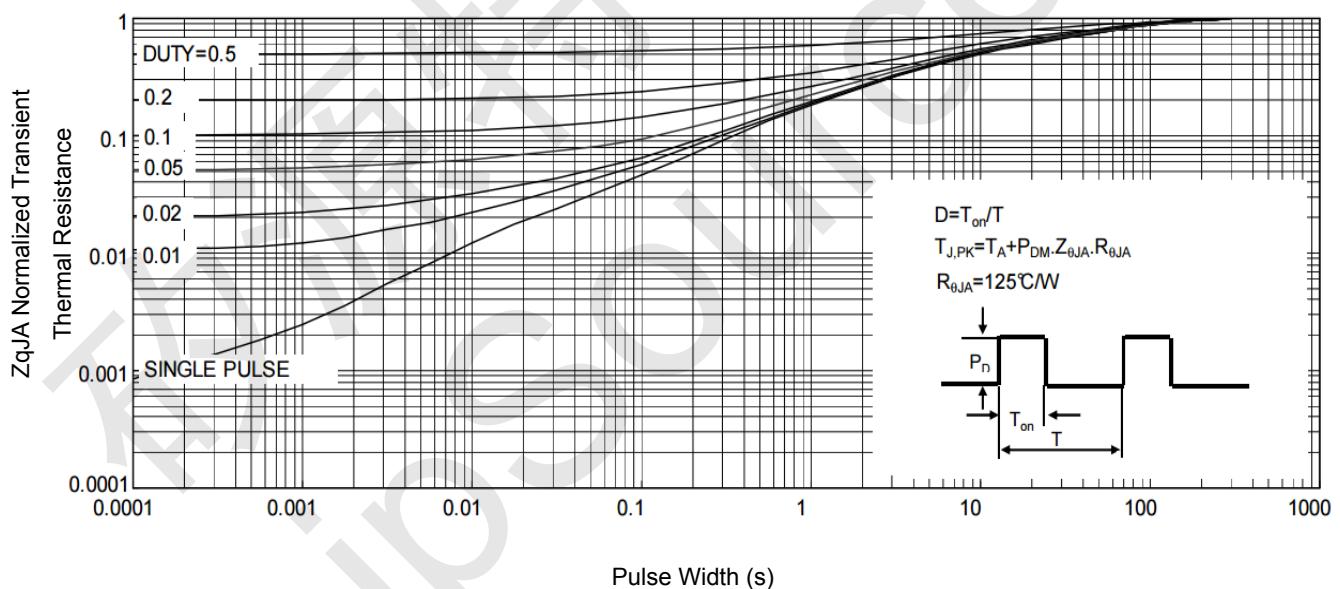


Fig9. Normalized Maximum Transient Thermal Impedance

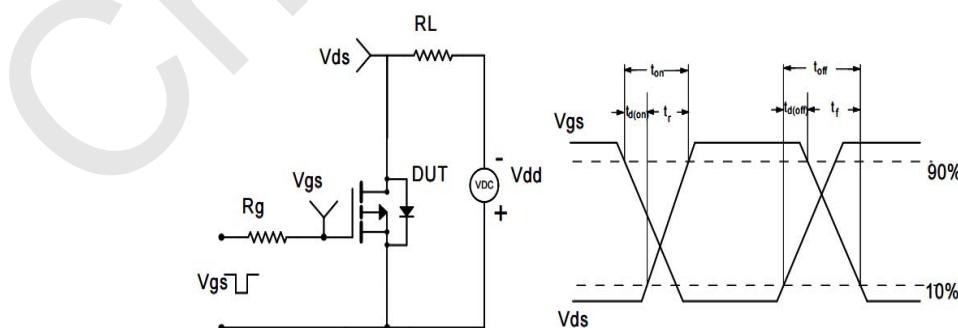
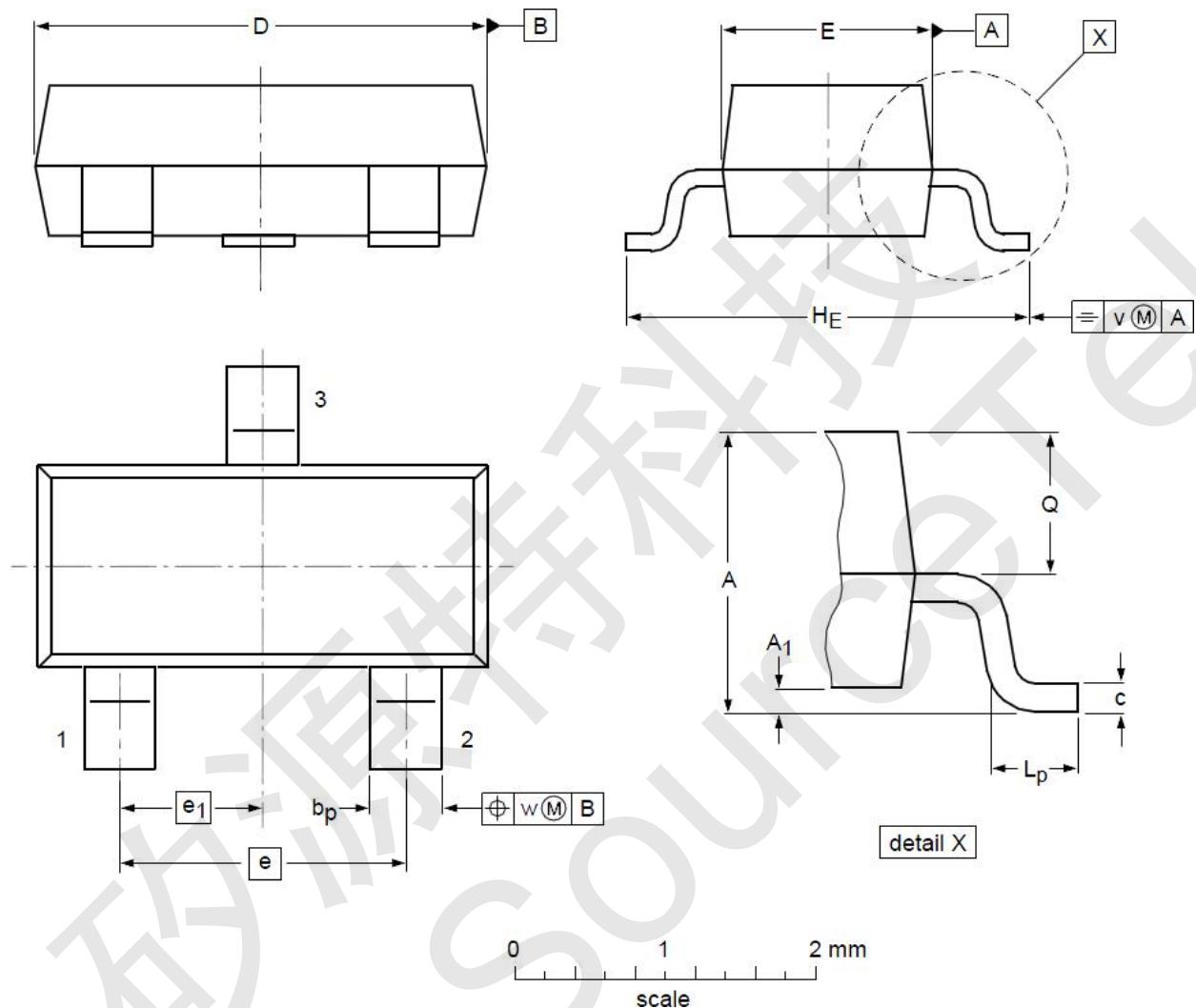


Fig10. Switching Time Test Circuit and waveforms



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CST2309 Package Mechanical Data-SOT-23



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A ₁	0.01	0.05	0.10
b _p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e ₁	--	0.95	--
H _E	2.25	2.40	2.55	L _p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				