



### CST20P70F P-Ch 18V Fast Switching MOSFETs

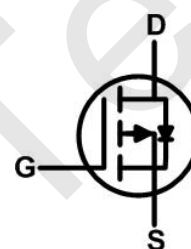
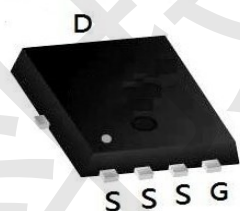
- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

#### CST20P70F Product Summary



BVDSS	RDSON	ID
-18V	3.6mΩ	-70A

#### CST20P70F PDFN5060-8L Pin Configuration



#### CST20P70F Description

The CST20P70F is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The CST20P70F meet the RoHS and Green Product requirement with full function reliability approved.

#### CST20P70F Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-18	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-70	A
$I_D@T_C=70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-53	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-280	A
$P_D@T_C=25^\circ C$	Total Power Dissipation <sup>3</sup>	62	W
$P_D@T_C=70^\circ C$	Total Power Dissipation <sup>3</sup>	35	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

#### CST20P70F Thermal Data

Symbol	Parameter	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	3	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup> (t $\leq 10s$ )		$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		$^\circ C/W$



### CST20P70F P-Ch 18V Fast Switching MOSFETs

#### CST20P70F Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250μA	-15	18	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V,	-	-	-1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.35	-0.65	-1.0	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A	-	3.6	5.5	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-12A	-	4.5	92	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f = 1.0MHz	-	6600	-	pF
C <sub>oss</sub>	Output Capacitance		-	460	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	659	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-10V, I <sub>D</sub> =-15A, V <sub>GS</sub> =-4.5V	-	76	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	10	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	20	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-10V, I <sub>D</sub> =-13A, R <sub>GEN</sub> =2.7Ω, V <sub>GS</sub> =-10V	-	14	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	130	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	187	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	190	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	-70	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-280	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> =-30A	-	-	-1.2	V
trr	Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>SD</sub> =-15A,	-	23	-	ns
Qrr	Reverse Recovery Charge	V <sub>GS</sub> =0V di/dt=-100A/μs	-	14	-	Nc

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=-10V, V<sub>G</sub>=-10V, R<sub>G</sub>=5.9Ω, L=0.5mh, I<sub>AS</sub>=-16A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



## CST20P70F Typical Performance Characteristics

Figure 1: Output Characteristics

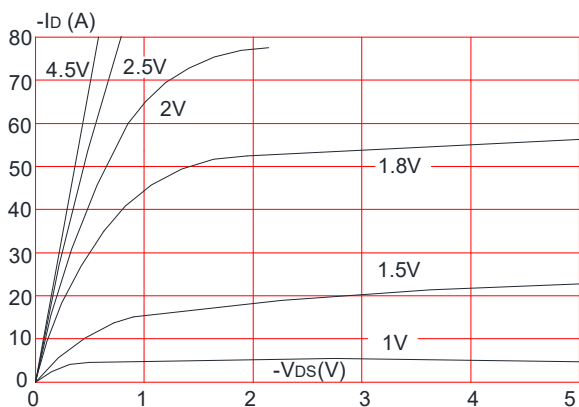


Figure 2: Typical Transfer Characteristics

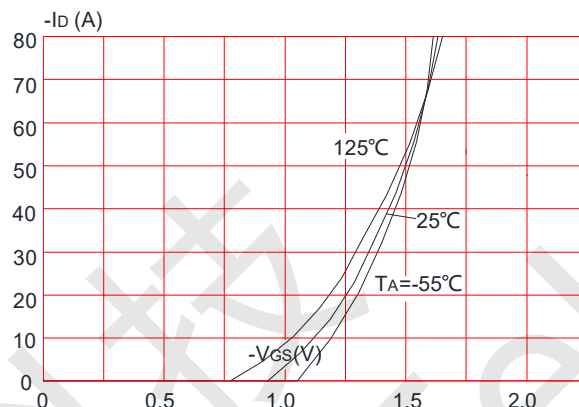


Figure 3: On-resistance vs. Drain Current

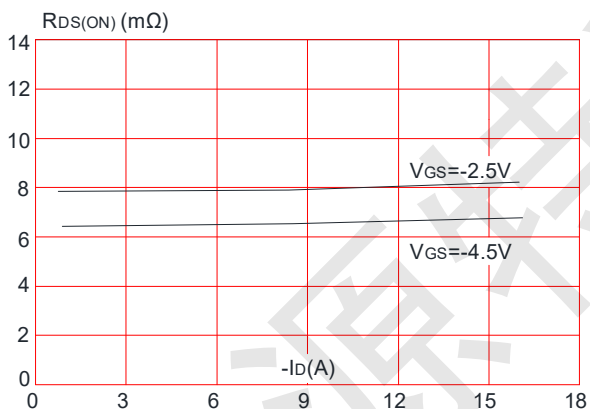


Figure 4: Body Diode Characteristics

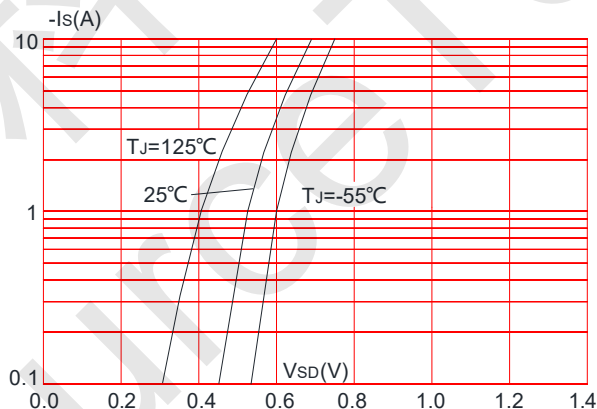


Figure 5: Gate Charge Characteristics

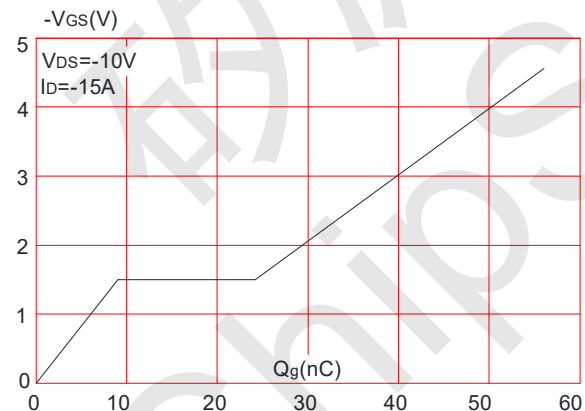
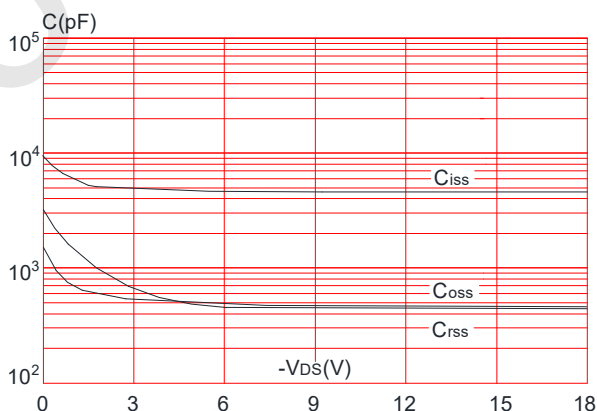


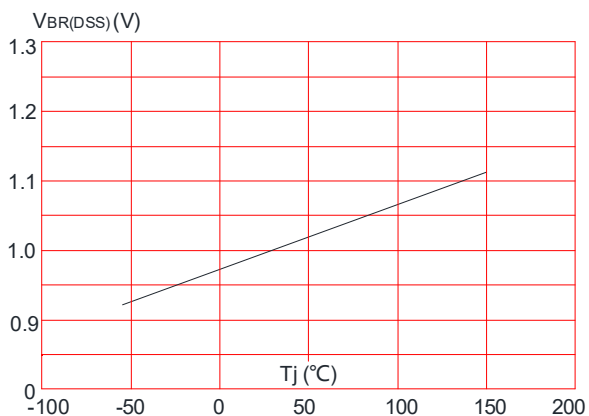
Figure 6: Capacitance Characteristics



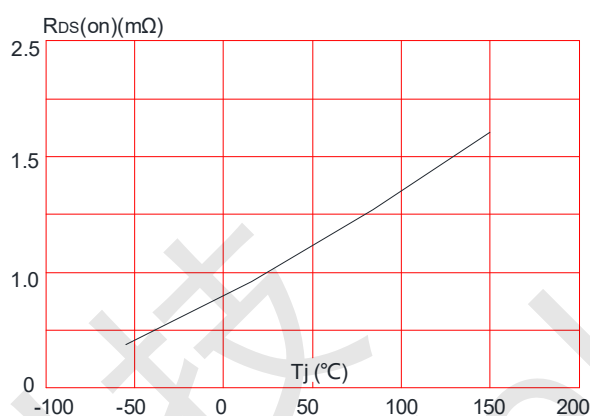


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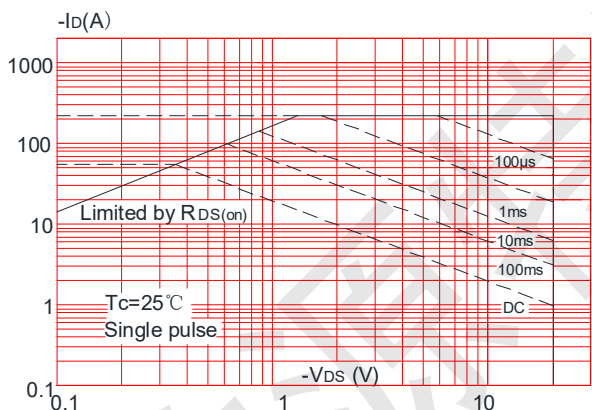
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



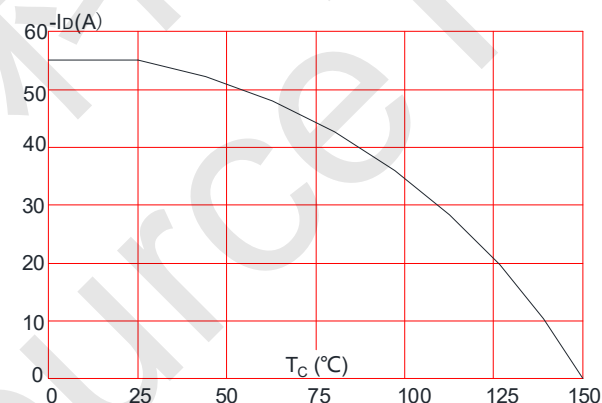
**Figure 8:** Normalized on Resistance vs. Junction Temperature



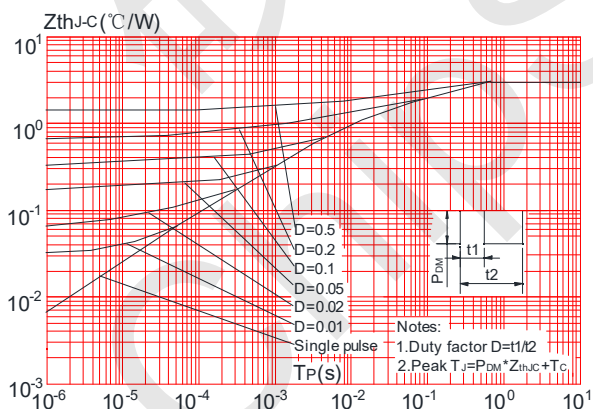
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

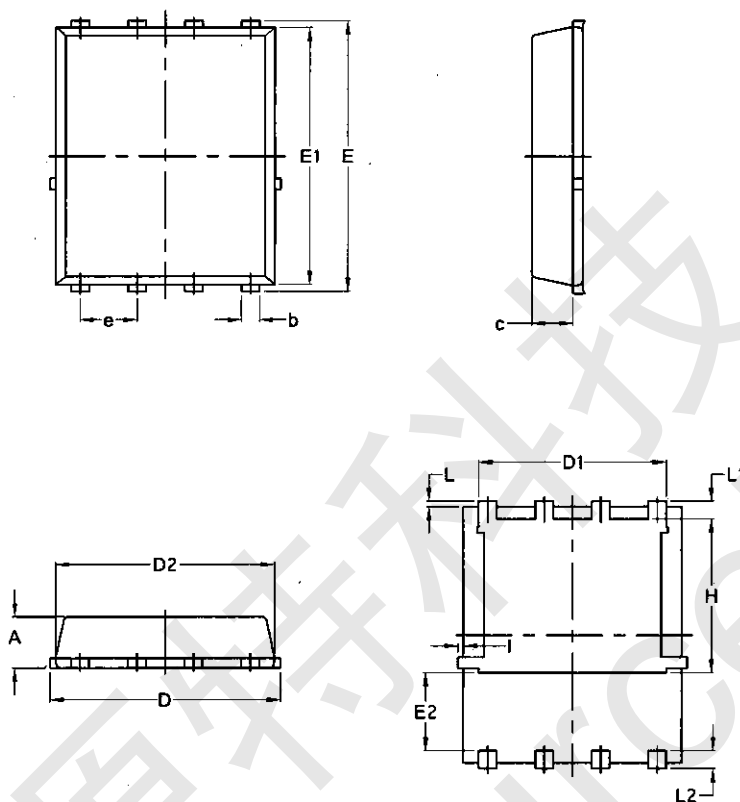


**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case





CST20P70F Package Mechanical Data-PDFN5060-8L-Single



Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070