



CST1216D P-Ch 15V Fast Switching MOSFETs

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

CST1216D Product Summary



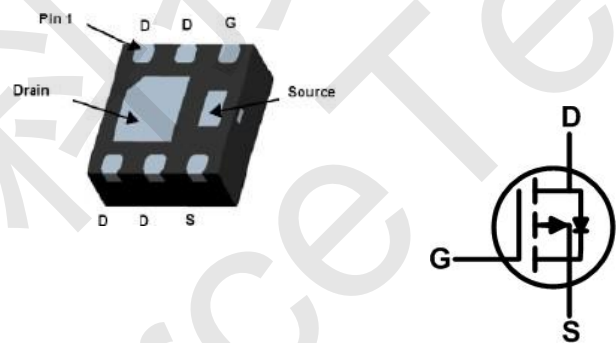
BVDSS	RDSON	ID
-15V	11.5mΩ	-16A

CST1216D Description

The CST1216D is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The CST1216D meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

CST1216D DFN 8-Pin Configuration



CST1216D Absolute maximum ratings (TC=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-15	V
Gate-Source Voltage	V_{GS}	±12	V
Drain Current-Continuous	I_D	-16	A
Drain Current -Pulsed (Note 1)	I_{DM}	-65	A
Maximum Power Dissipation (TC=25°C)	$P_D (T_C=25^\circ C)$	18	W
Maximum Power Dissipation (TA=25°C)	$P_D (T_A=25^\circ C)$	2.5	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

CST1216D Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	6.9	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	50	°C/W



CST1216D P-Ch 15V Fast Switching MOSFETs

CST1216D Electrical characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-15	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.4	-0.7	-1	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-6.7A	-	11.5	18	mΩ
		V _{GS} =-2.5V, I _D =-6.2A	-	14	22	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-6.7A	20	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C _{iss}	V _{DS} =-10V, V _{GS} =0V, F=1.0MHz	-	2700	-	PF
Output Capacitance	C _{oss}		-	680	-	PF
Reverse Transfer Capacitance	C _{riss}		-	590	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-10V, I _D =-1A V _{GS} =-4.5V, R _{GEN} =10Ω	-	11	-	nS
Turn-on Rise Time	t _r		-	35	-	nS
Turn-Off Delay Time	t _{d(off)}		-	30	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Q _g	V _{DS} =-6V, I _D =-10A, V _{GS} =-4.5V	-	35	48	nC
Gate-Source Charge	Q _{gs}		-	5	-	nC
Gate-Drain Charge	Q _{gd}		-	10	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =-8A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S		-	-	-16	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production



CST1216D Typical Electrical and Thermal Characteristics

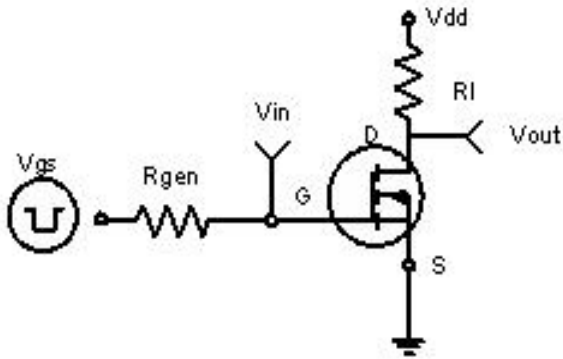


Figure 1: Switching Test Circuit

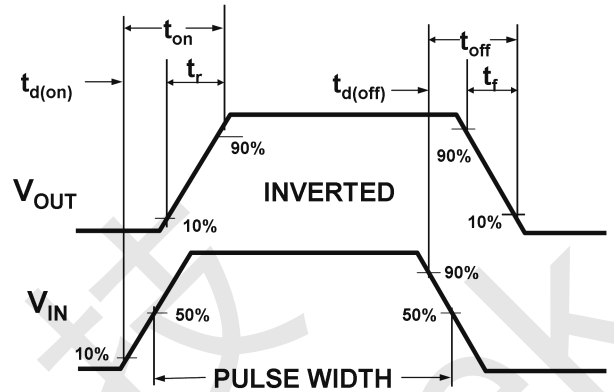


Figure 2: Switching Waveforms

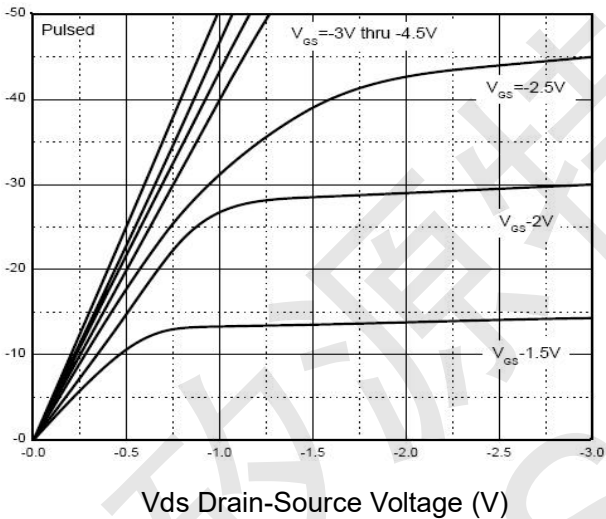


Figure 3 Output Characteristics

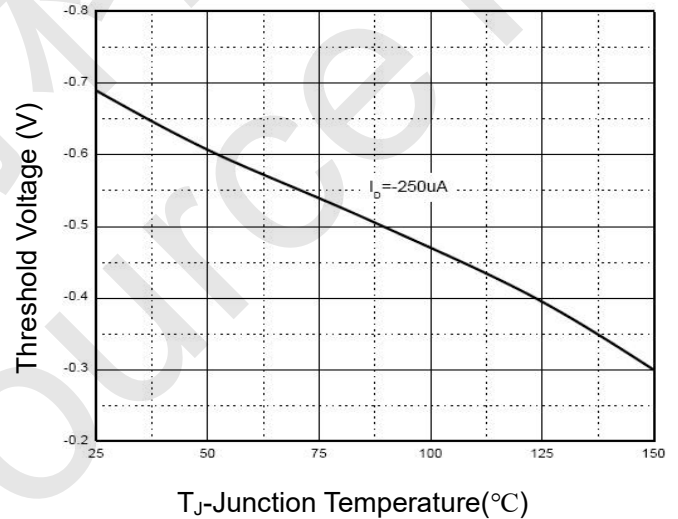


Figure 4 Drain Current

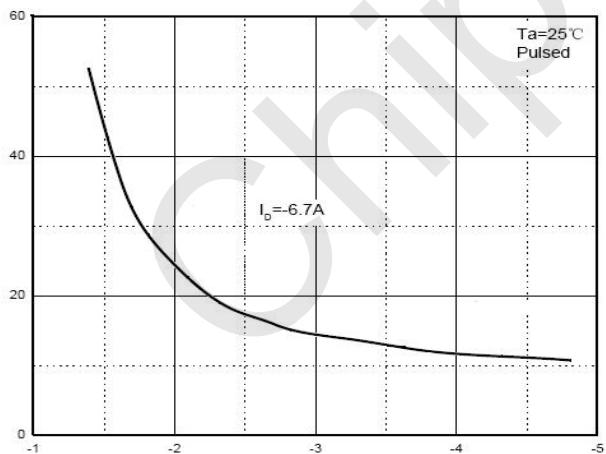


Figure 5 Rdson vs Vgs

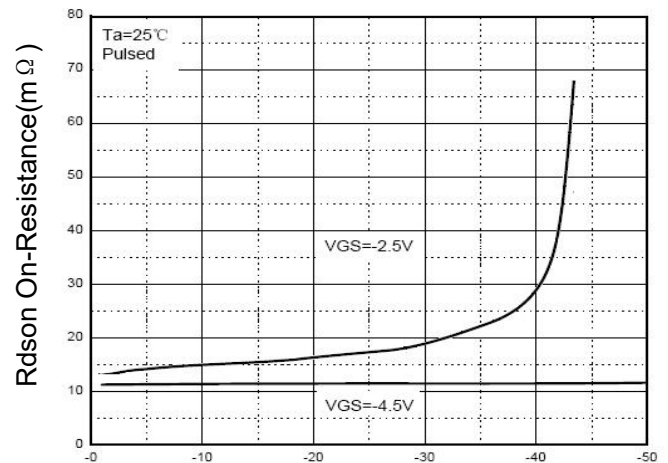
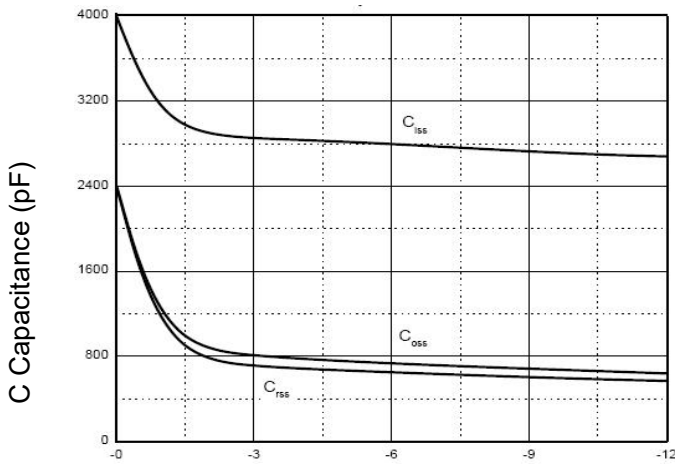
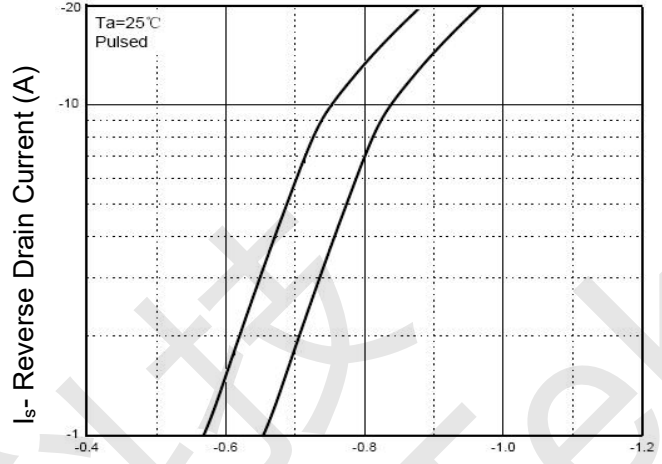


Figure 6 Drain-Source On-Resistance



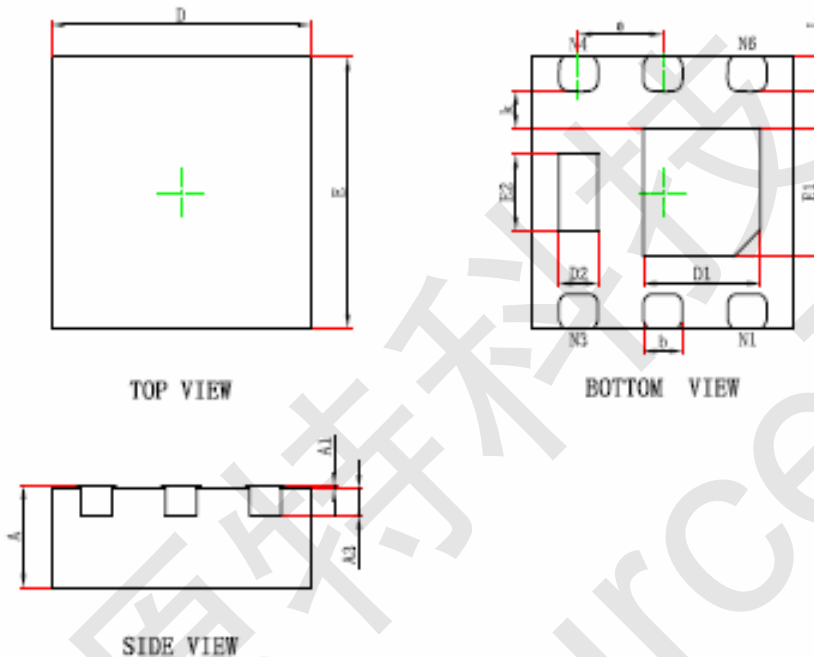
Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds



Vsd Source-Drain Voltage (V)
Figure 8 Source- Drain Diode Forward



CST1216D DFN2020-6L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.924	2.076	0.076	0.082
E	1.924	2.076	0.076	0.082
D1	0.800	1.000	0.031	0.039
E1	0.850	1.050	0.033	0.041
D2	0.200	0.400	0.008	0.016
E2	0.460	0.660	0.018	0.026
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
L	0.174	0.326	0.007	0.013